

	1	2	3	4	5	6	7	8																																																				
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E									E																																																			
F									F																																																			
								<table><tr><td colspan="2">DRAWER</td><td>EE</td><td>DATE</td><td>POWER</td><td>DATE</td><td rowspan="4">INVENTEC</td></tr><tr><td colspan="2">DESIGN</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="2">CHECK</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="2">RESPONSIBLE</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="2">DATE</td><td>CHANGE NO.</td><td>REV</td><td colspan="3">SIZE = 3</td><td>VER :</td><td><table><tr><td>SIZE</td><td>CODE</td><td>DOC. NUMBER</td><td>REV</td></tr><tr><td>A3</td><td>CS</td><td></td><td>AXI</td></tr></table></td></tr><tr><td colspan="2">FILE NAME : XXXX-XXXXXX-XX</td><td colspan="3">P/N</td><td colspan="3">XXXXXXXXXXXX</td><td>SHEET 1 OF 52</td></tr></table>		DRAWER		EE	DATE	POWER	DATE	INVENTEC	DESIGN						CHECK						RESPONSIBLE						DATE		CHANGE NO.	REV	SIZE = 3			VER :	<table><tr><td>SIZE</td><td>CODE</td><td>DOC. NUMBER</td><td>REV</td></tr><tr><td>A3</td><td>CS</td><td></td><td>AXI</td></tr></table>	SIZE	CODE	DOC. NUMBER	REV	A3	CS		AXI	FILE NAME : XXXX-XXXXXX-XX		P/N			XXXXXXXXXXXX			SHEET 1 OF 52
DRAWER		EE	DATE	POWER	DATE	INVENTEC																																																						
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FILE NAME : XXXX-XXXXXX-XX		P/N			XXXXXXXXXXXX			SHEET 1 OF 52																																																				
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MOBILE THREE

ES2 BUILD

2011.02.08

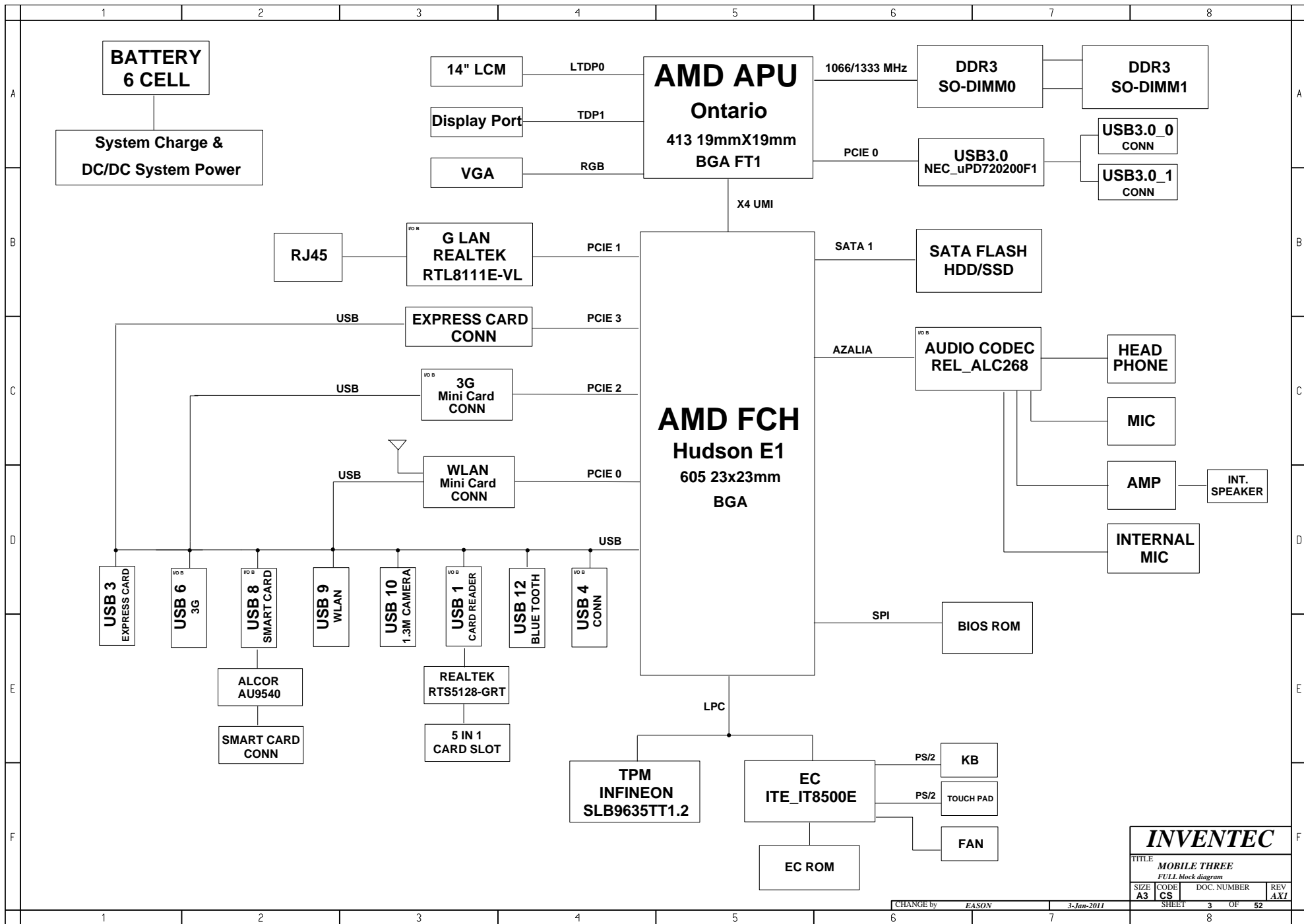
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2. INDEX	21. FCH2	40. CODEC REL_ALC268
3. BLOCK DIAGRAM	22. FCH3	41. AUDIO JACK
4. POWER BLOCK DAIGRAM	23. FCH4	42. AMP / INT. SPEAKER
5. POWER ON SEQUENCE	24. STRAP OPTION / THERMAL / RTC	43. WWAN
6. POWER ON SEQUENCE	25. LCM	44. SMART CARD AU9540
7. POWER CHARGE/DC IN	26. CRT	45. CARD READER
8. POWER BATTERY	27. DP	46. GIGA LAN
9. POWER +V3LA/+V3A/+V5A	28. WLAN / BT	47. RJ45
10. POWER +V1.8S/+V0.75S/+V1.5	29. TPM	48. USB CONN
11. POWER +V1.1A/+V1A/+V1.1S	30. SSD / HDD	49. WIRE to BOARD CONN (I/O/B SIDE)
12. POWER VCORE/VCORE_NB	31. IT8500E	TP BOARD
13. POWER +V5S/+V3S/+V1.1S	32. KB/TP CONN	50. TP / HALL-SENSOR
14. APU FT1 1	33. USB3.0 CONTROLLER	POWER BOARD
15. APU FT1 2	34. USB3.0 CONN	51. POWER SW
16. APU FT1 3	35. SIM CARD	52. EMI
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18. DDR3-1	37. FAN CONN	
19. DDR3-2	38. WIRE to BOARD CONN (MB SIDE)	
	39. SCREWS	

INVENTEC

TITLE MOBILE THREE			
index			
SIZE A3	CODE CS	DOC. NUMBER	REV AXI
SHEET		2 OF	52

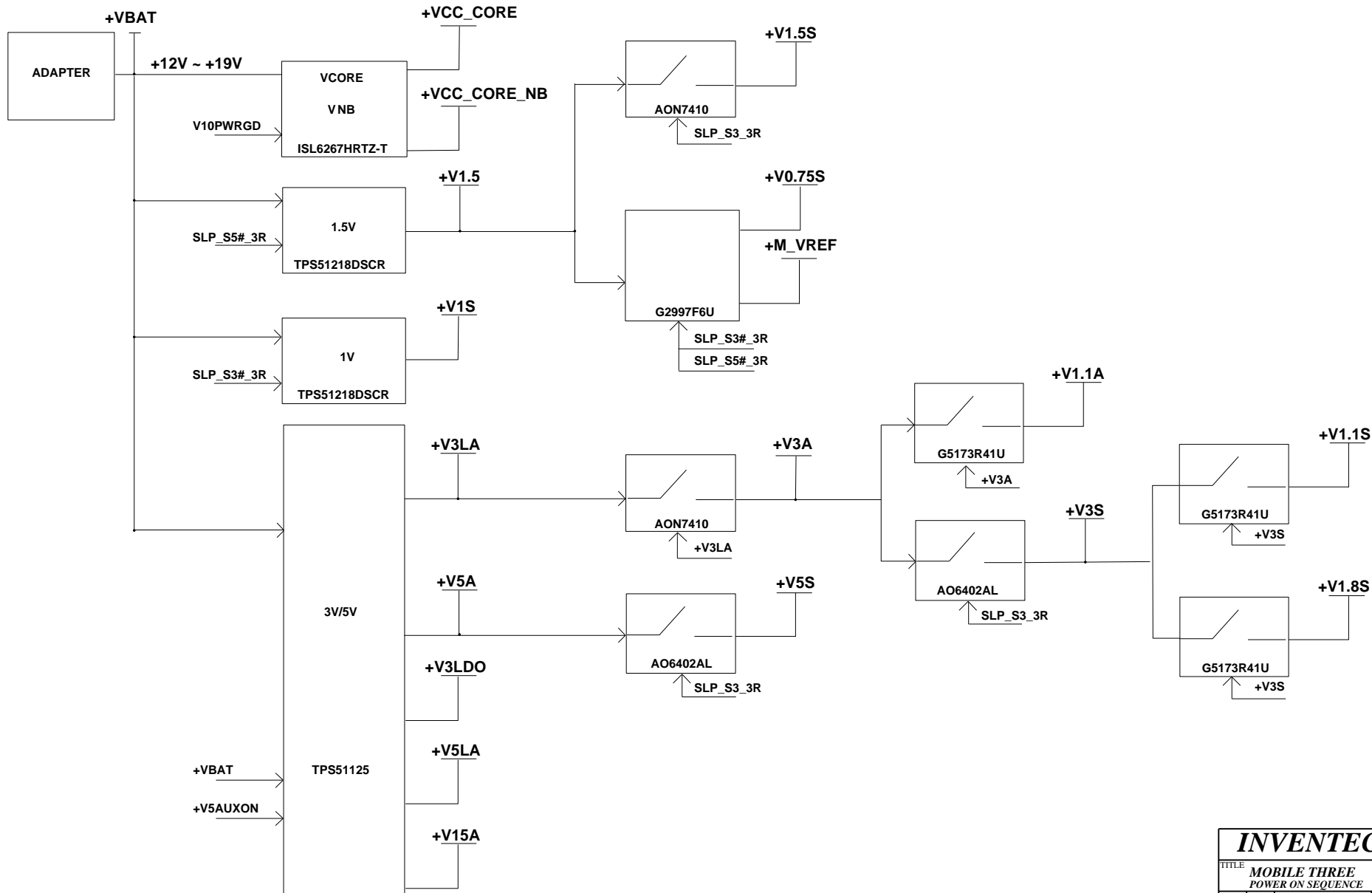
CHANGE by **EASON** 3-Jan-2011



INVENTEC

TITLE		
MOBILE THREE		
FULL block diagram		
SIZE	CODE	DOC. NUMBER
A3	CS	REV
SHEET		AXI

Power Block Diagram



INVENTEC

TITLE MOBILE THREE POWER ON SEQUENCE			
SIZE A3	CODE CS	DOC. NUMBER SHEET 4 OF 52	REV AXI

CHANGE by **EASON** 3-Jan-2011

Timing diagram for POWER ON SEQ. showing signals S5 and S0.

Signals (Y-axis):

- FCH_PWRGD
- APU_VRM_PWRGD
- +VDDCR_NB
- +VDDCR_CPU
- +V1.1S
- +VDD_1.0
- +V1.8S
- +M_VTT
- +APU_VDDIO_SUS
- +V12S
- +V3S
- +V5S
- SLP_5S#_3R
- SLP_S3#_3R
- PWR_BTN#
- RSMRST#
- +V1.1A
- +V3A
- +V5A
- +V3LA
- +V5LA
- +VBAT

Timing constraints:

- 10ms < t < 30ms (indicated for PWR_BTN# and RSMRST#)

Legend:

- S5
- S0

Footer:

CHANGE by: EASON 3-Jan-2011

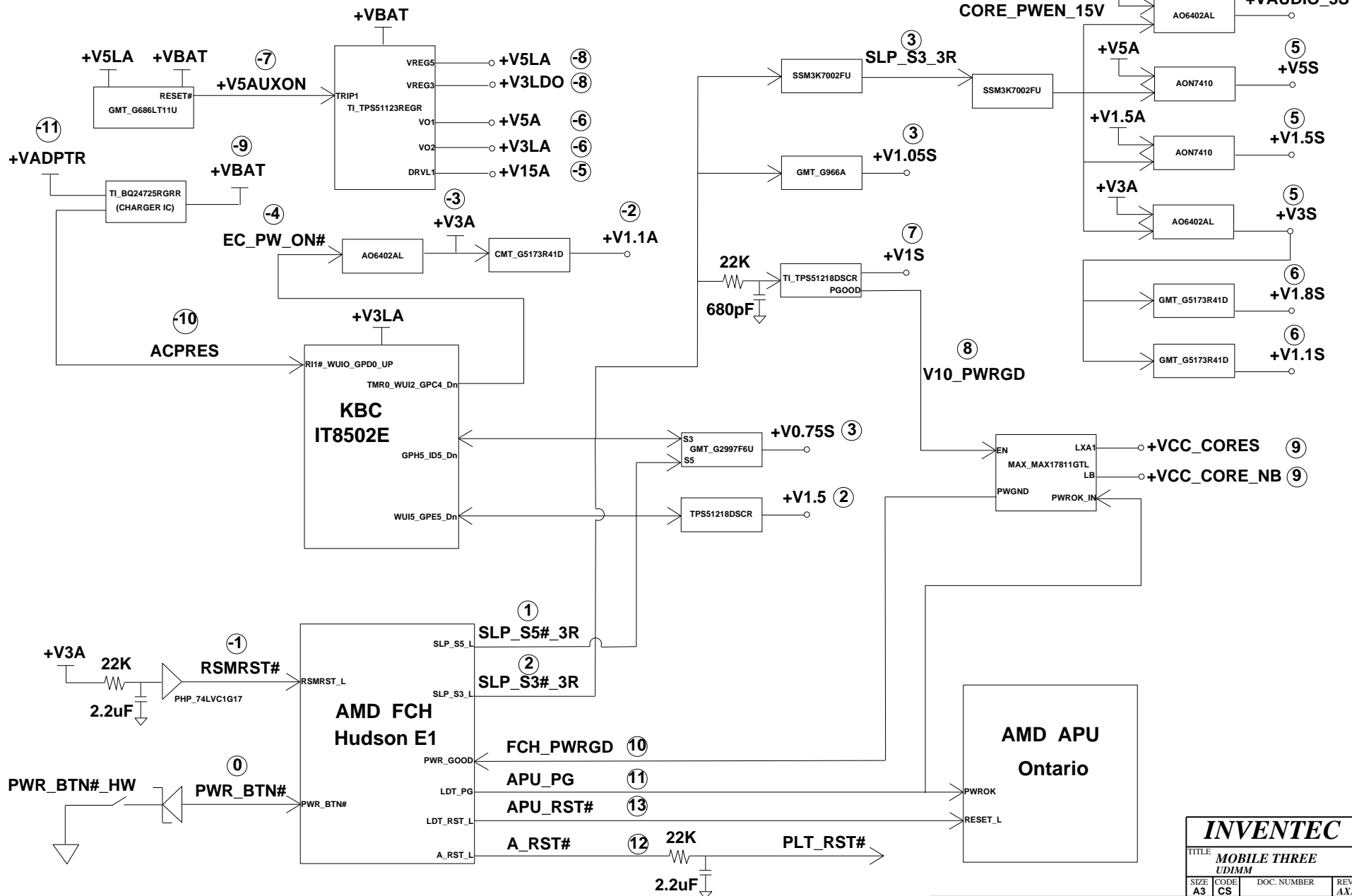
SHEET 5 OF 52

INVENTEC

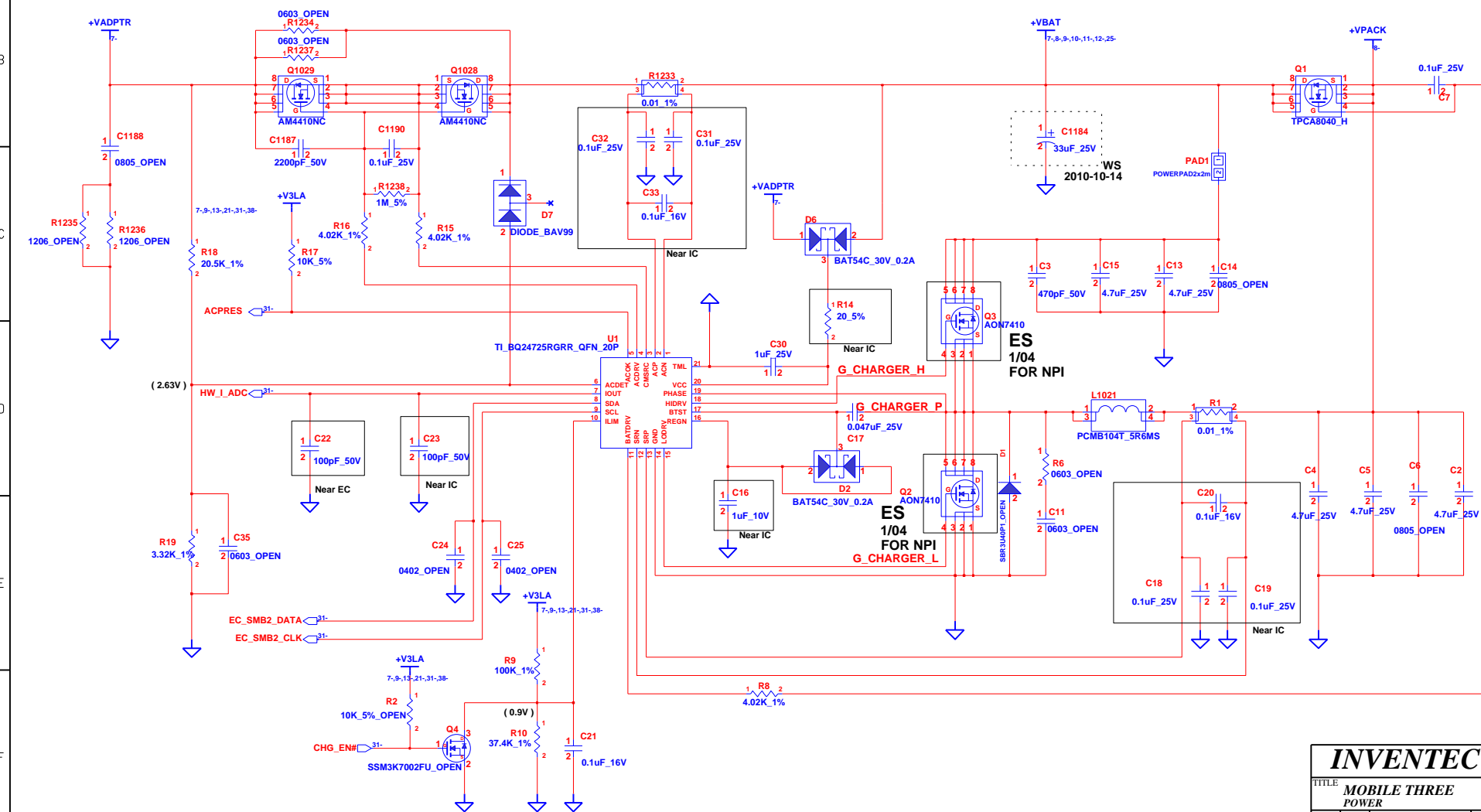
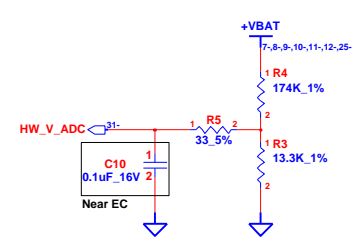
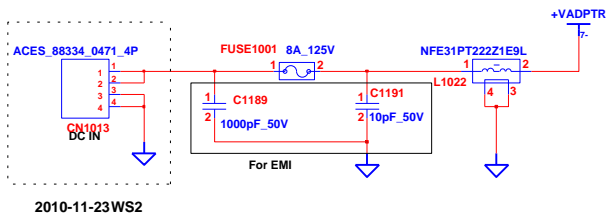
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SIZE: A3 CODE: CS DOC. NUMBER: REV: AXI

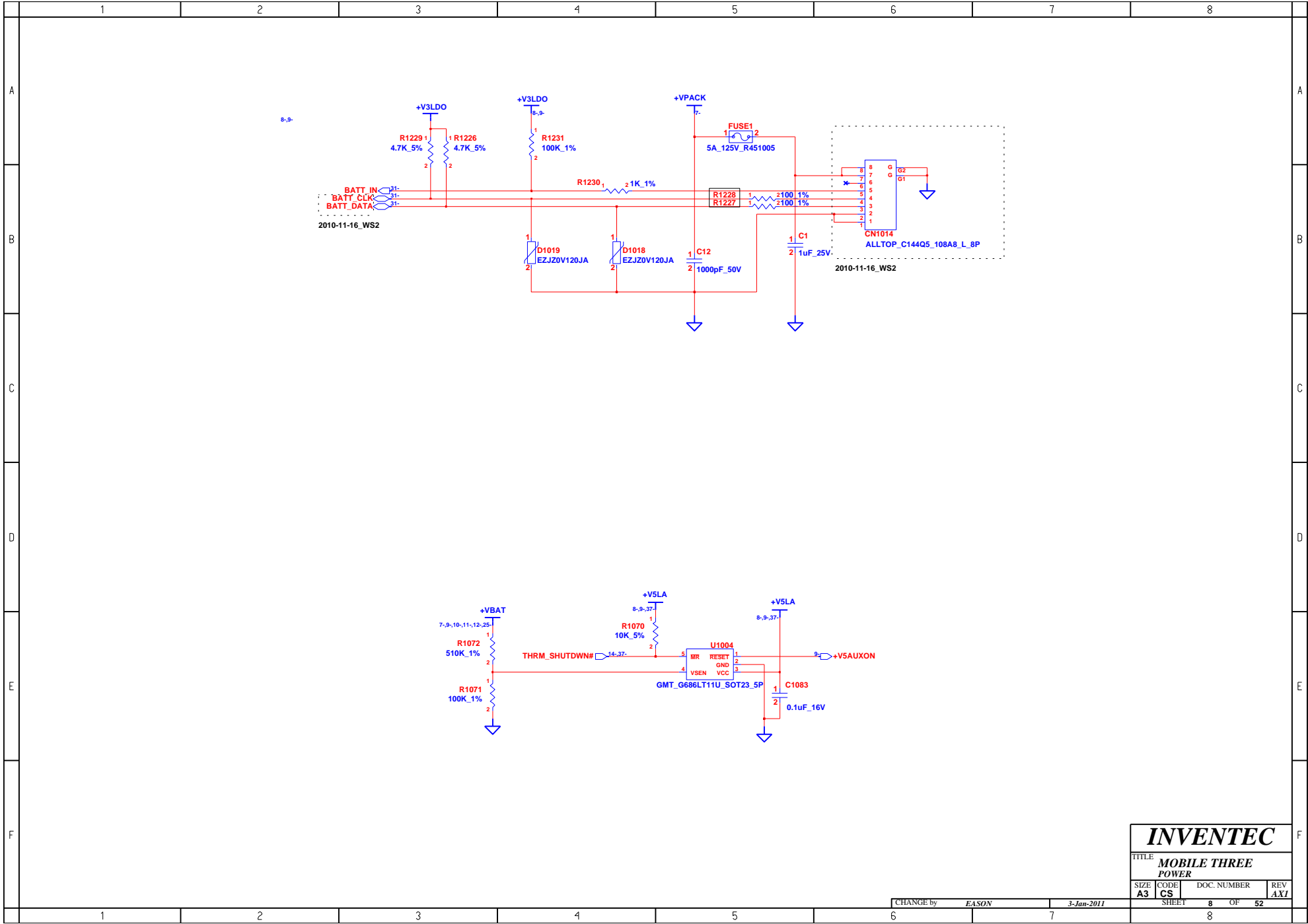
POWER SEQUENCE



FUSE6000
65W-75W 8A(6036A0003401)
90W 10A(6036A0002901)
120W 12A(6036A0006001)



INVENTEC			
TITLE: MOBILE THREE POWER			
SIZE: A3	CODE: CS	DOC. NUMBER: 7	REV: AXI
CHANGE by: EASON		3-Jan-2011	
SHEET: 7		OF 52	



INVENTEC

TITLE
**MOBILE THREE
POWER**

SIZE A3	CODE CS	DOC. NUMBER	REV AXI
SHEET		8 OF	52

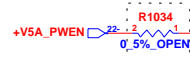
CHANGE by **EASON** 3-Jan-2011

+V3LA +V3A +V5A

WS
11/04



WS
11/05

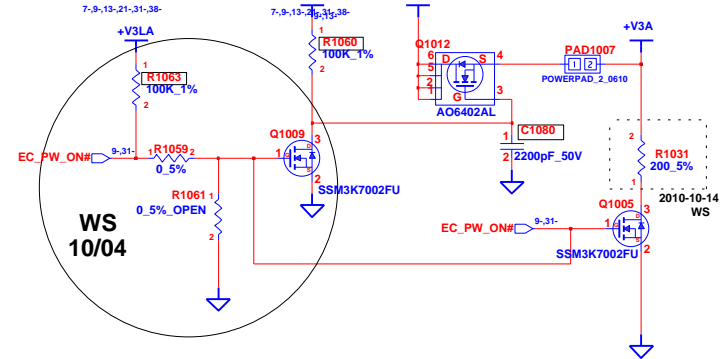


DIODES_2N7002DW_SOT363_6P
C1039
0402_OPEN

2010-10-26
WS



WS
10/04

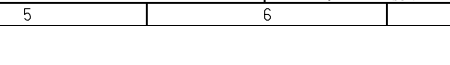
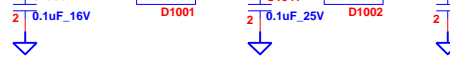
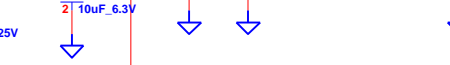
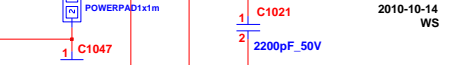
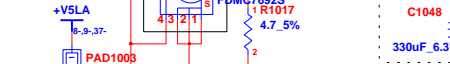
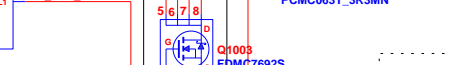
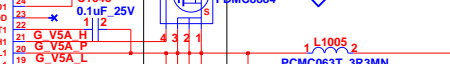
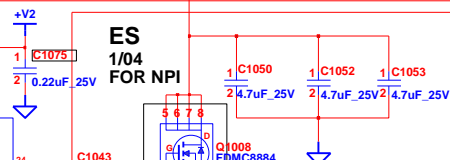


+VBAT

7,8,9,10,11,12,25-

PAD1005
POWERPAD_2_0610

ES
1/04
FOR NPI



>>VRE3 OR VRE5=00A
SKIPSEL>>VREF=ASKIP
>>GND=PWM

>>VRE5=365/460
>>VRE3=300/375
>>VREF=245/305
>>GND=200/250

INVENTEC			
TITLE MOBILE THREE POWER			
SIZE A3	CODE CS	DOC. NUMBER	REV AXI
SHEET		9	OF 52

CHANGE by EASON 3-Jan-2011

1

2

3

4

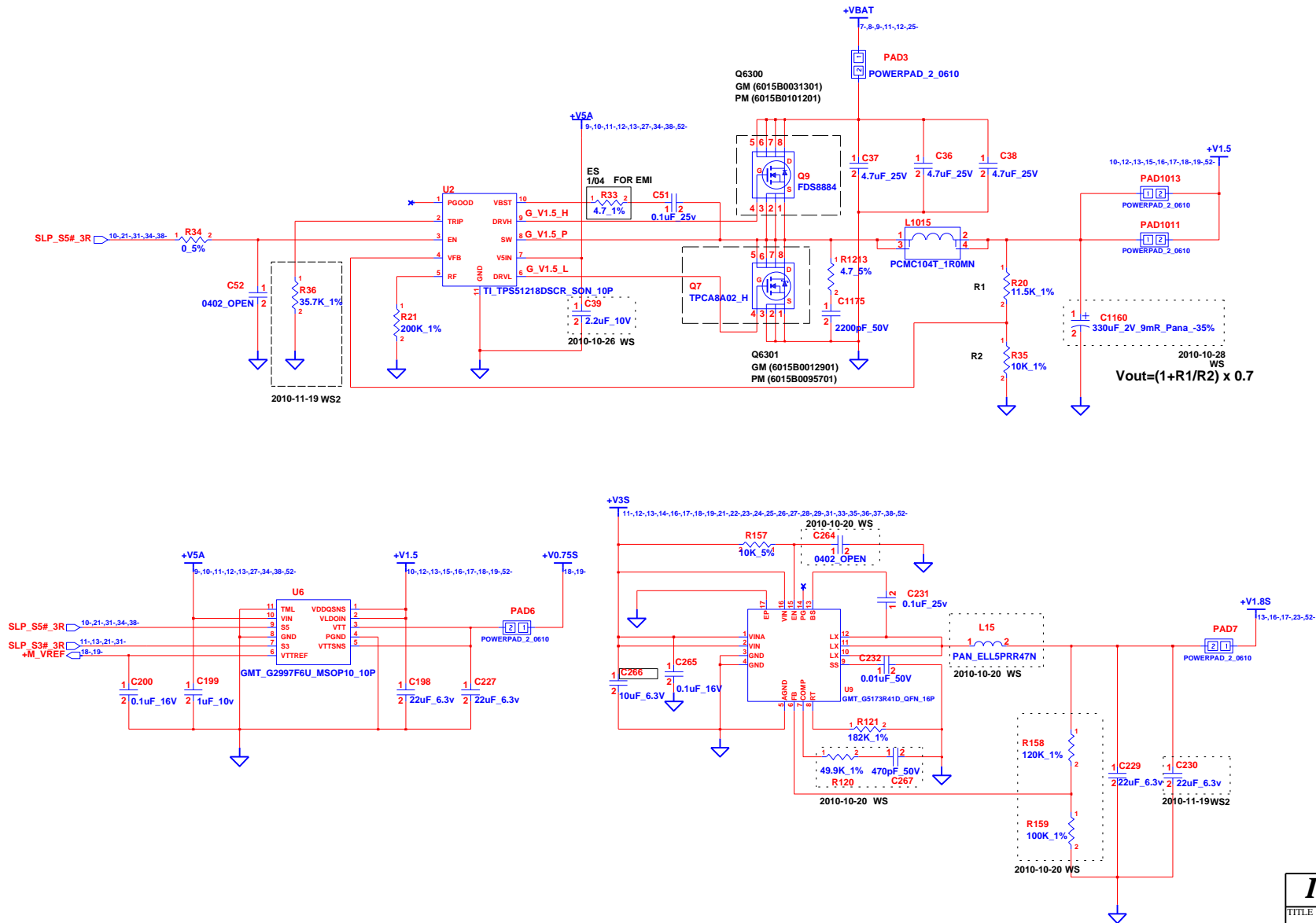
5

6

7

8

+V1.8S +V0.75S +V1.5

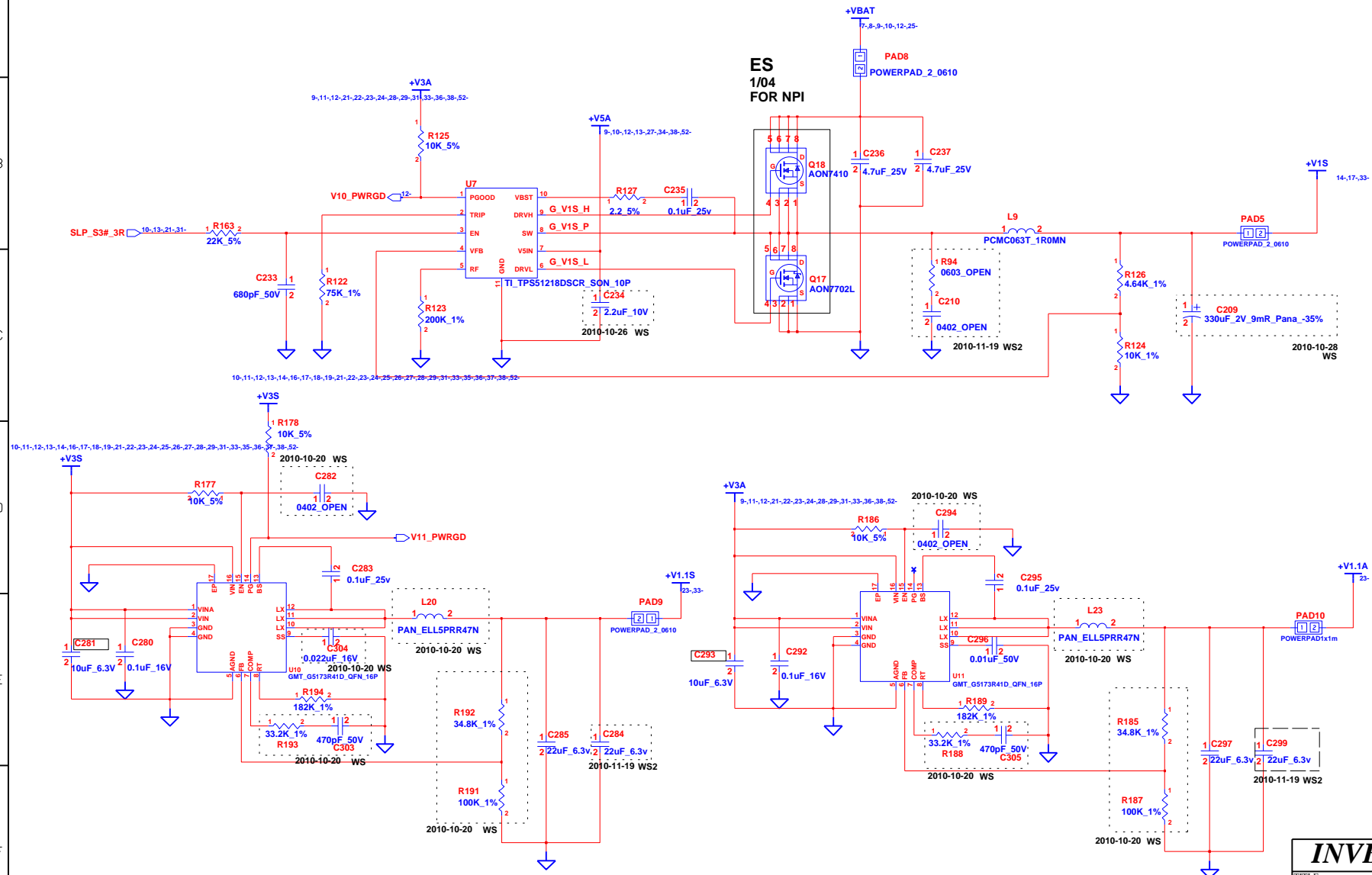


INVENTEC

TITLE MOBILE THREE POWER			
SIZE A3	CODE CS	DOC. NUMBER	REV AXI
SHEET		10	OF 52

CHANGE by EASON 3-Jan-2011

+V1.1A +V1S +V1.1S

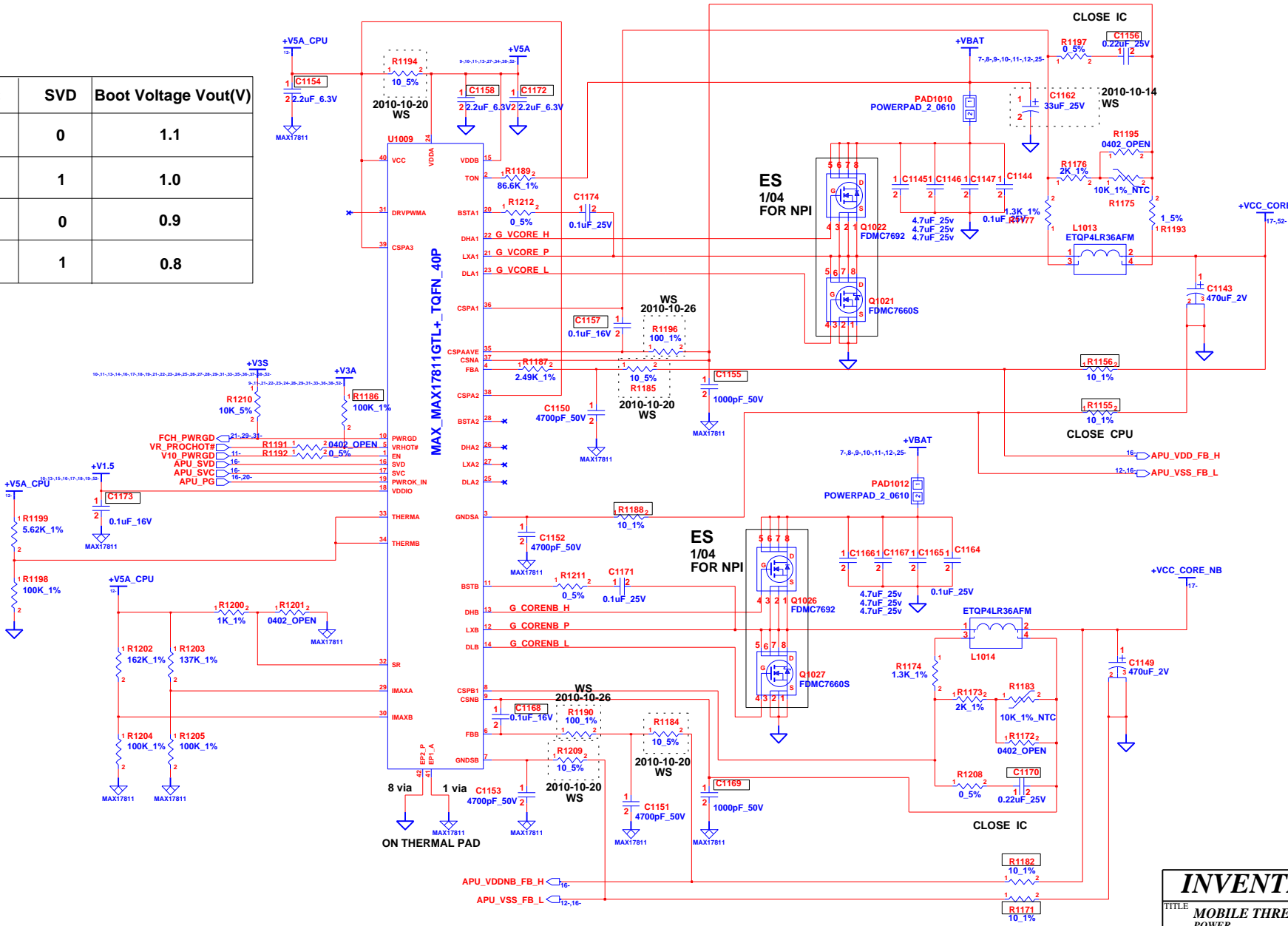


INVENTEC

TITLE MOBILE THREE POWER			
SIZE A3	CODE CS	DOC. NUMBER	REV AXI
SHEET		11 OF	52

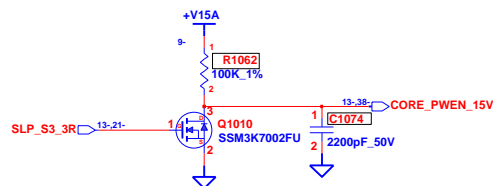
CHANGE by EASON 3-Jan-2011

SVC	SVD	Boot Voltage Vout(V)
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

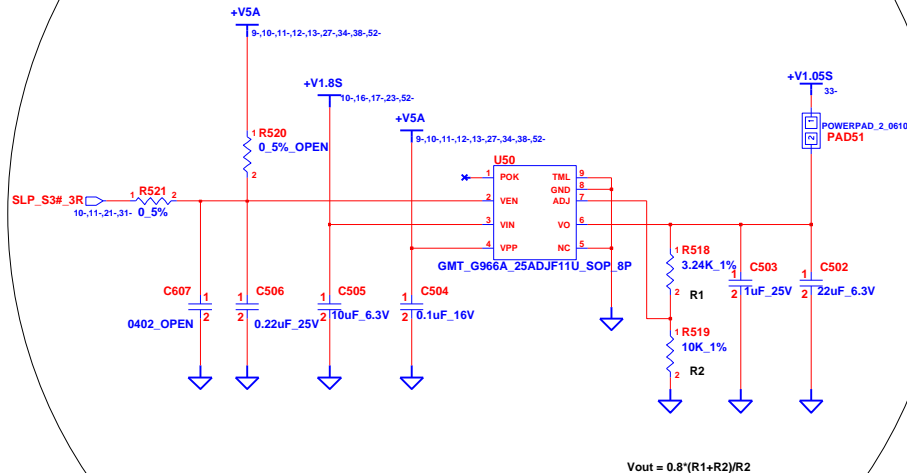


TITLE **MOBILE THREE
POWER**

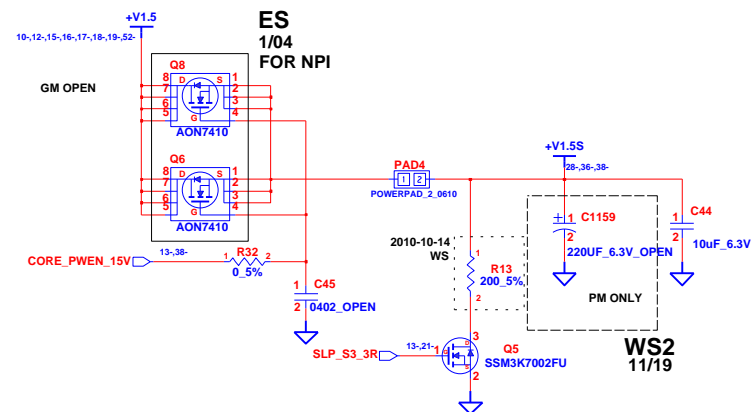
+V5S +V3S +V1.5S +V1.05S +VAUDIO_5S



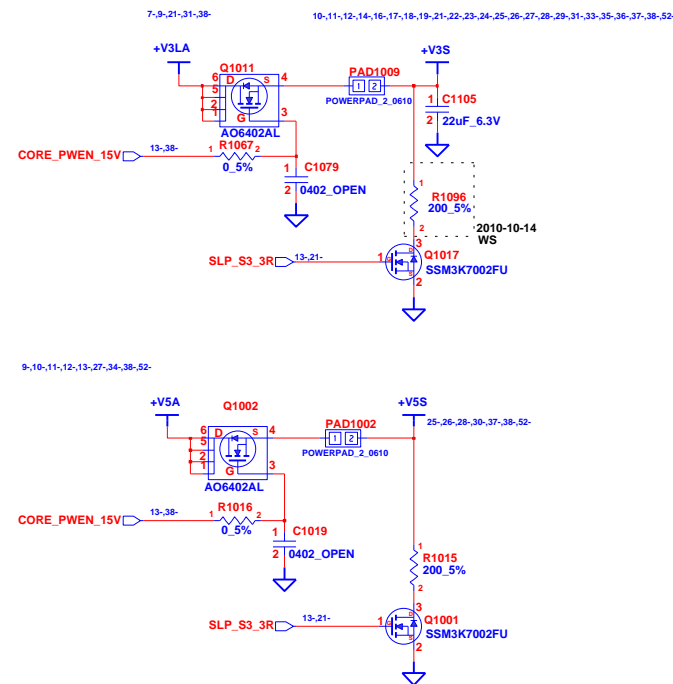
WS2
11/25



$$V_{out} = 0.8 \cdot (R1 + R2) / R2$$



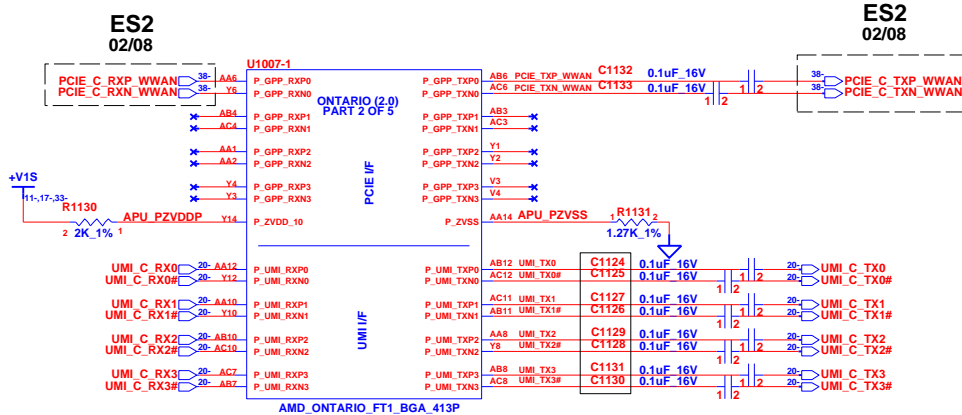
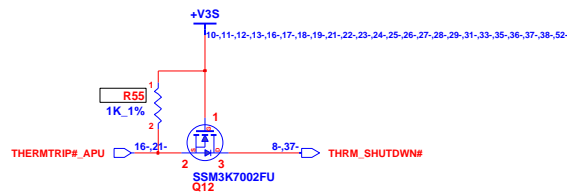
WS2
11/19



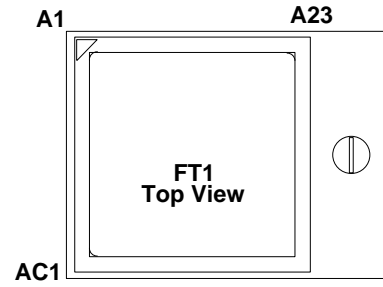
INVENTEC

TITLE MOBILE THREE POWER			
SIZE A3	CODE CS	DOC. NUMBER	REV AXI
SHEET		13	OF 52

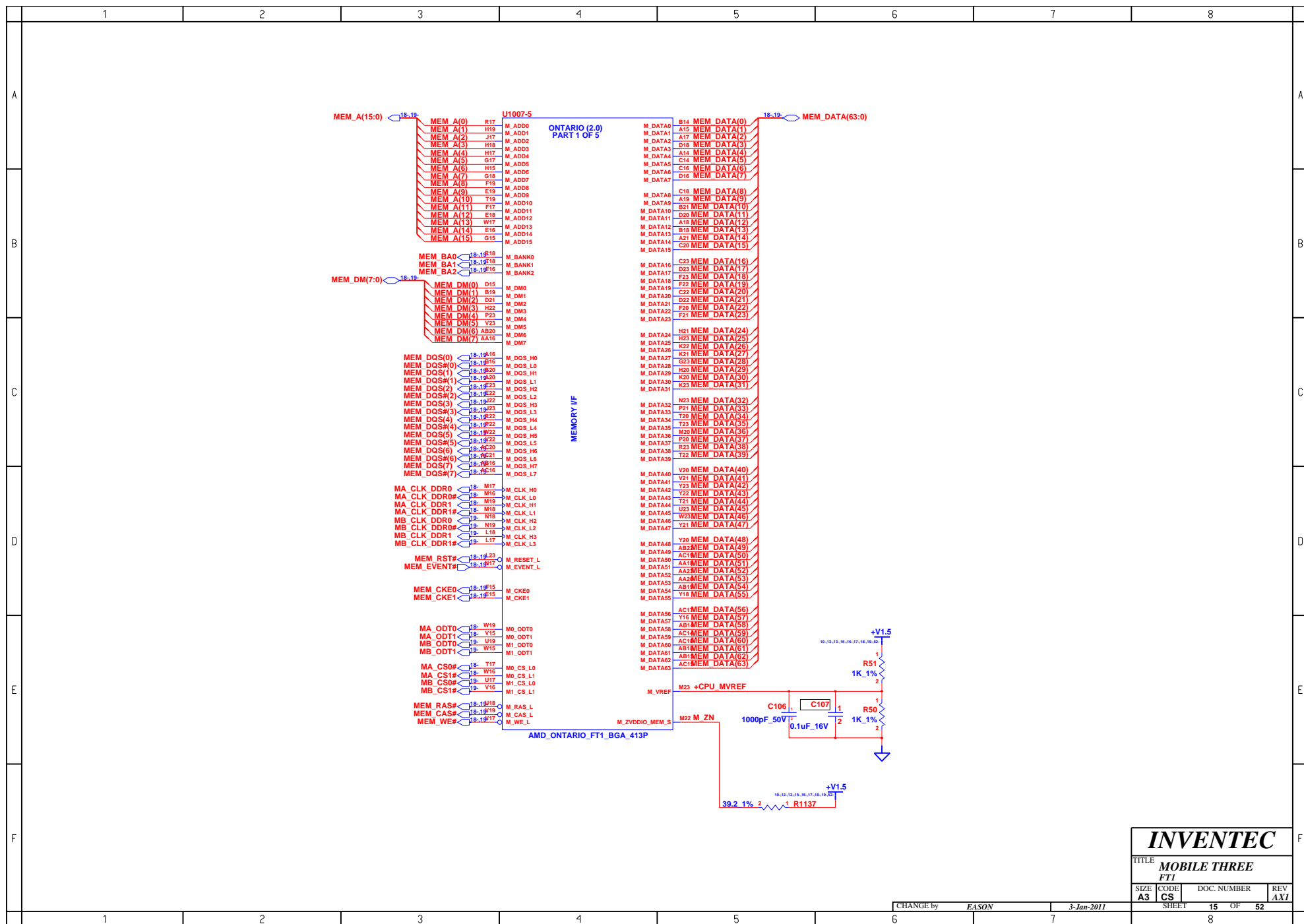
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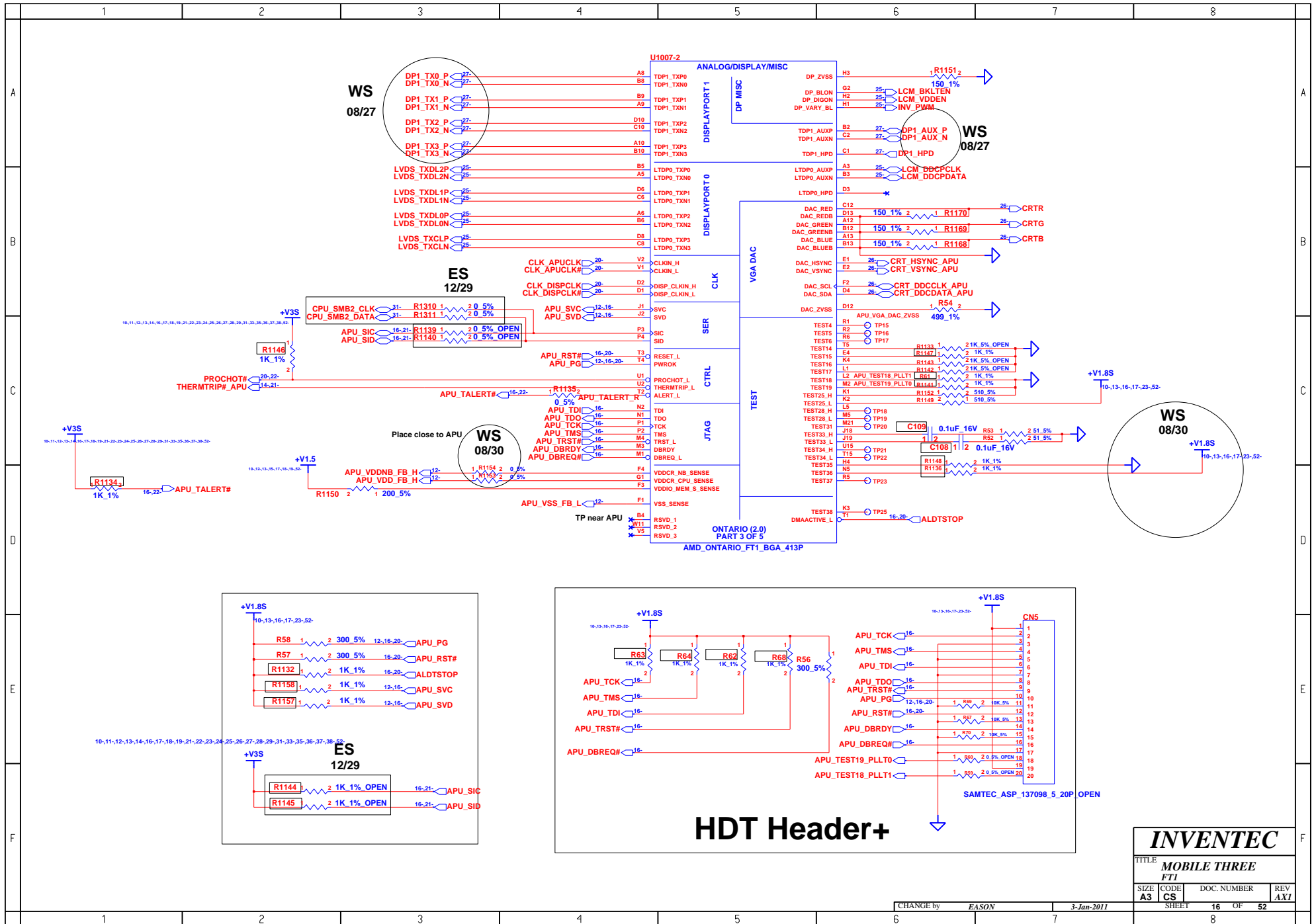


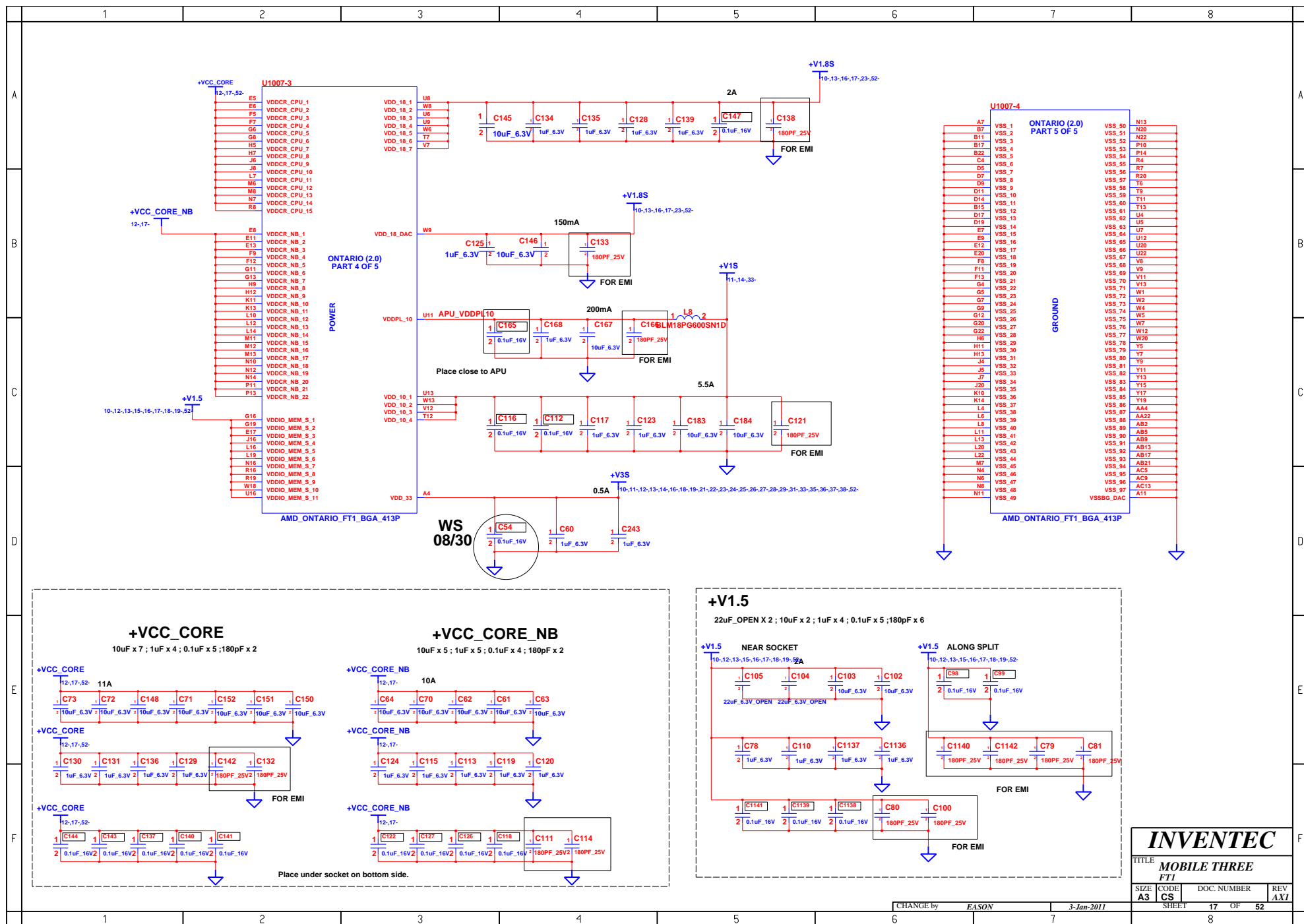
Layout: Add stitching caps if crossing plane split.



INVENTEC			
TITLE: MOBILE THREE FT1			
SIZE: A3	CODE: CS	DOC. NUMBER: 14	REV: AXI
CHANGE by: EASON		3-Jan-2011	
SHEET: 14		OF: 52	
8			







DDR3

SO DIMM0
SLOT A(REVERSE)

INVENTEC

TITLE
MOBILE THREE
DDR3-1

SIZE
A3

CODE
CS

DOC. NUMBER

REV
AX1

CHANGE by EASON 3-Jan-2011

SHEET 18 OF 52

8

DDR3

SO DIMM0

SLOT A(REVERSE)

JAE_MM80_204B1_D9R_DT_204P

CN1007-1

CN1007-2

WS 10/05

330uF 2.5V OPEN

+V1.5

+V3S

+M_VREF

+V0.75S

MEM_A(15:0)

MEM_DATA(63:0)

MEM_DM(7:0)

MEM_DQS(7:0)

MEM_DQS#(7:0)

MEM_EVENT#

MEM_RST#

VREF_DO

VREF_CA

VSS1

VSS2

VSS3

VSS4

VSS5

VSS6

VSS7

VSS8

VSS9

VSS10

VSS11

VSS12

VSS13

VSS14

VSS15

VSS16

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VSS46

VSS47

VSS48

VSS49

VSS50

VSS51

VSS52

VTT1

VTT2

G1

G2

NC1

NC2

NCTEST

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VT197

VT198

VT199

VT200

VT201

VT202

VT203

VT204

DDR3

SO DIMM0

SLOT A(REVERSE)

INVENTEC

MOBILE THREE

DDR3-1

SIZE A3

CODE CS

DOC. NUMBER

REV AX1

CHANGE by EASON 3-Jan-2011

SHEET 18 OF 52

DDR3

SO DIMM0

SLOT A(REVERSE)

INVENTEC

MOBILE THREE

DDR3-1

SIZE A3

CODE CS

DOC. NUMBER

REV AX1

CHANGE by EASON 3-Jan-2011

SHEET 18 OF 52

[illegible][illegible]

DDR3

SO DIMM0

SLOT A(REVERSE)

INVENTEC

MOBILE THREE

DDR3-1

SIZE A3

CODE CS

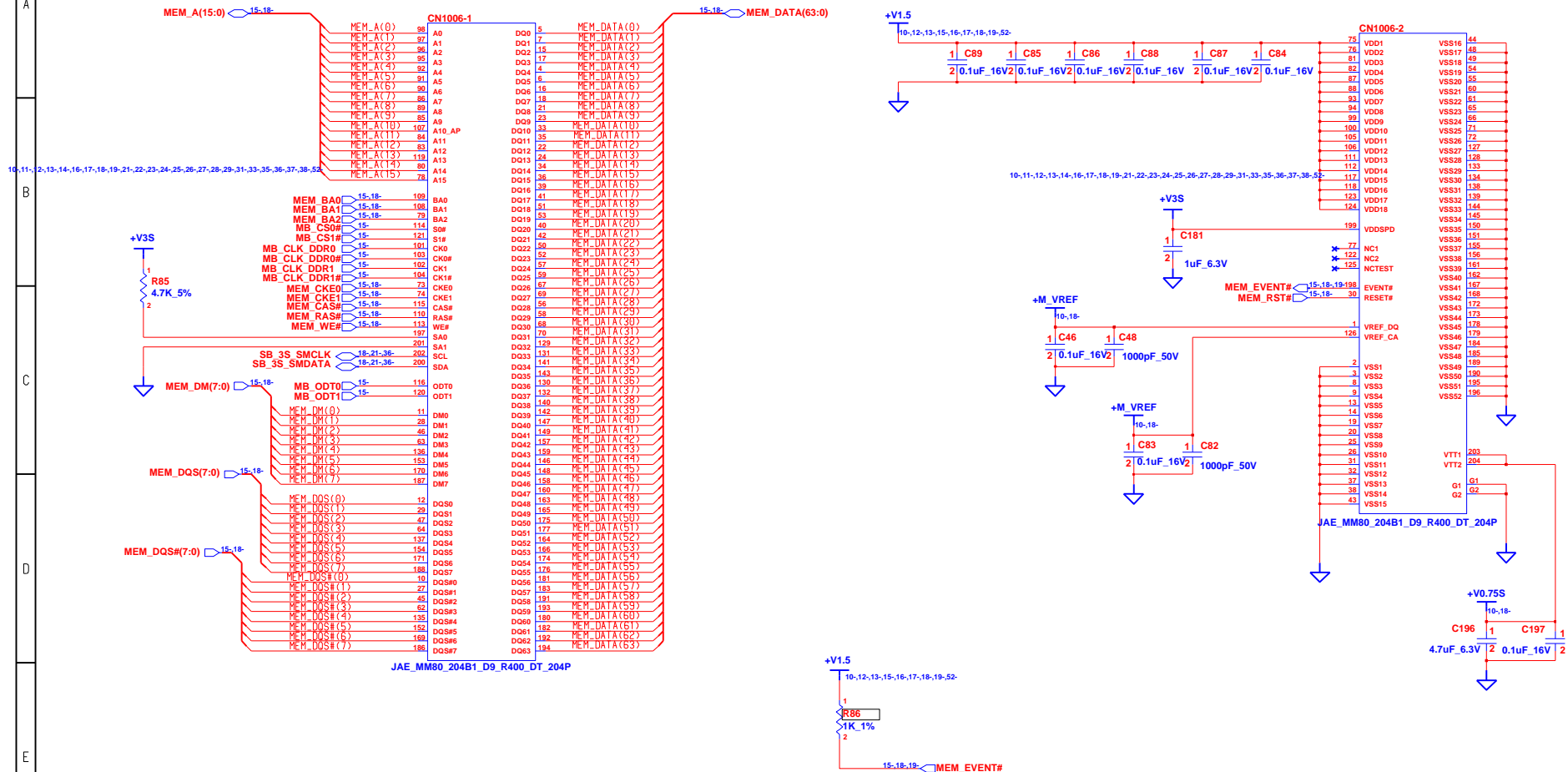
DOC. NUMBER

REV AX1

CHANGE by EASON 3-Jan-2011

SHEET 18 OF 52

DDR3

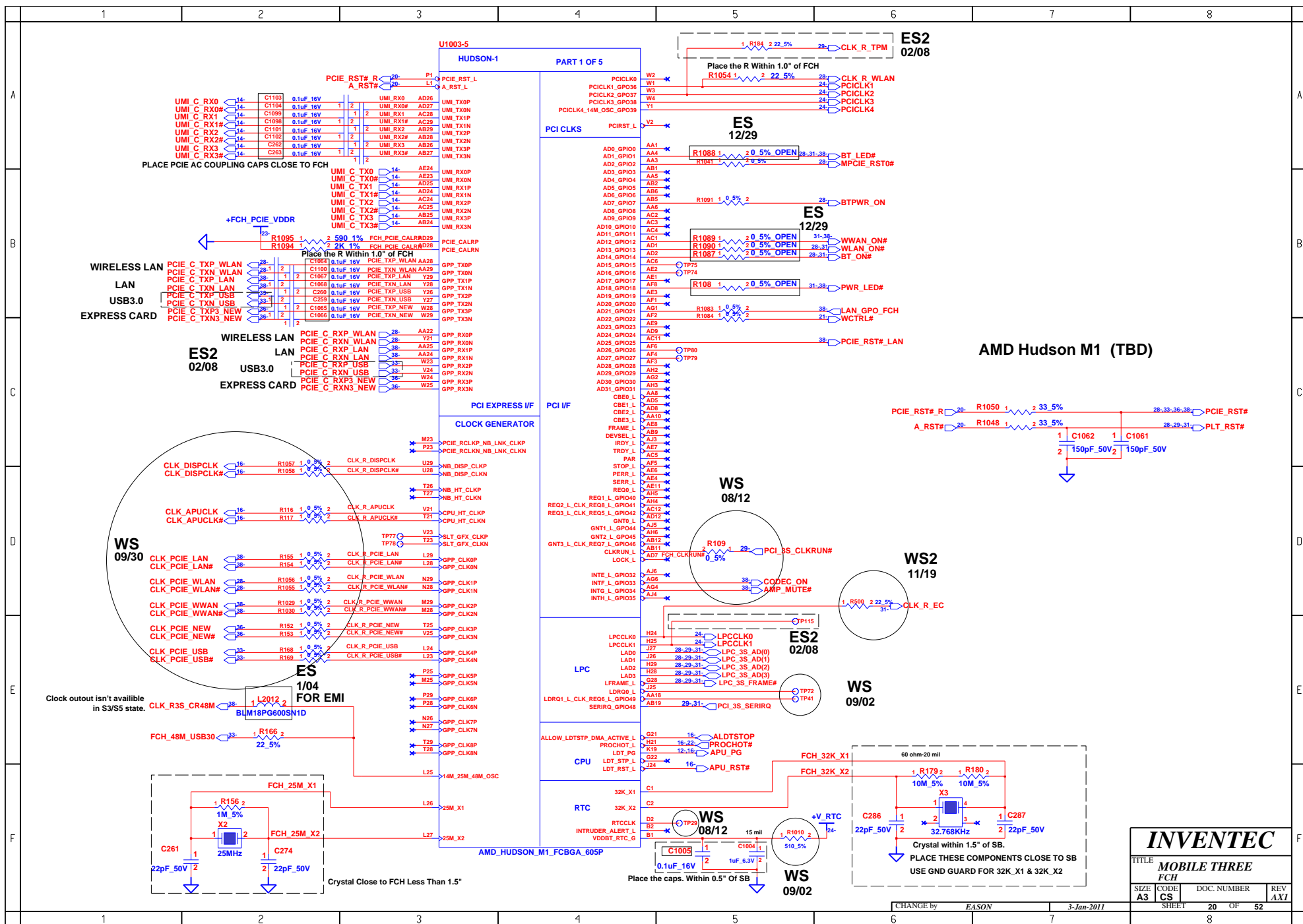


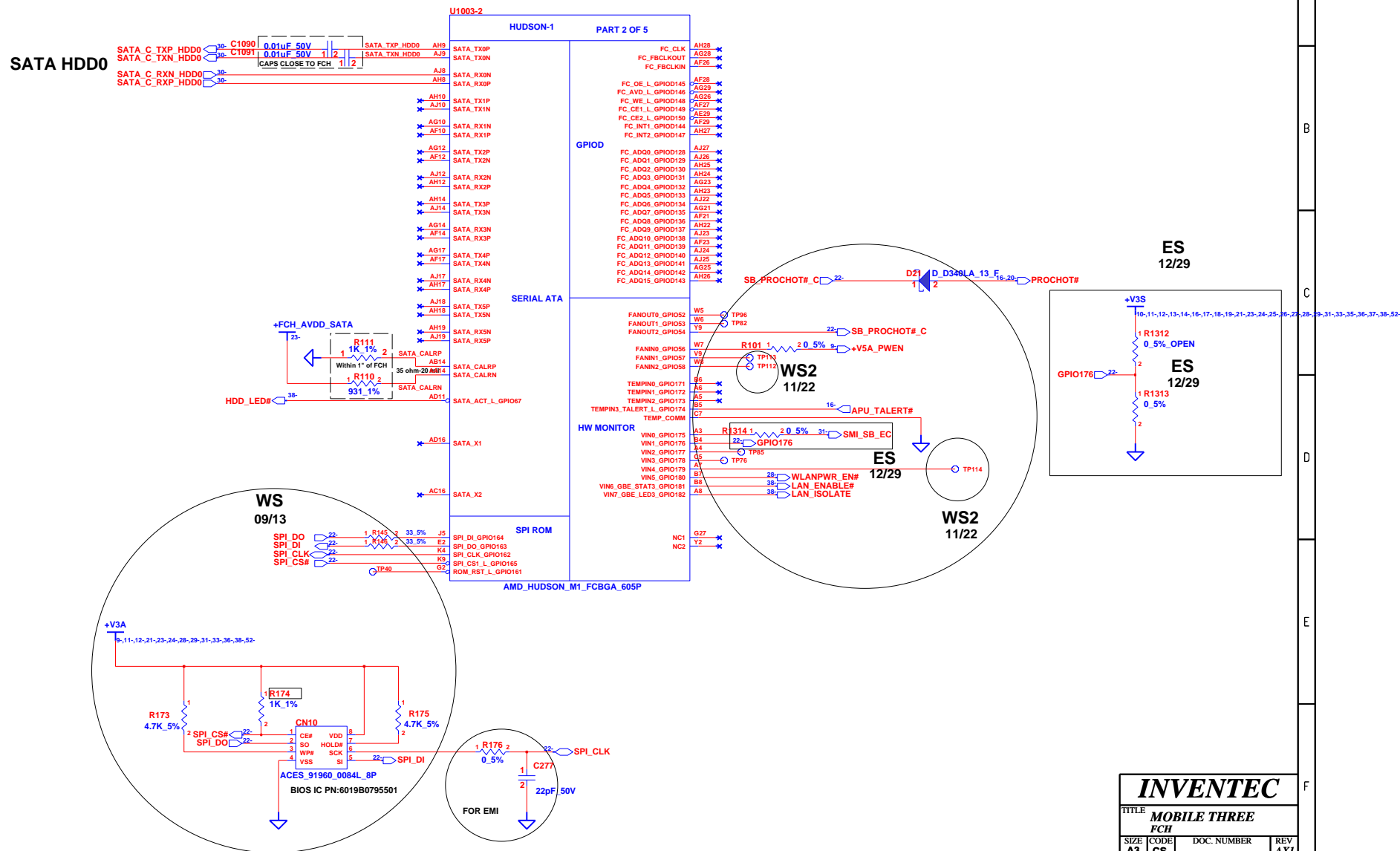
SO DIMM1
SLOT B(STANDARD)

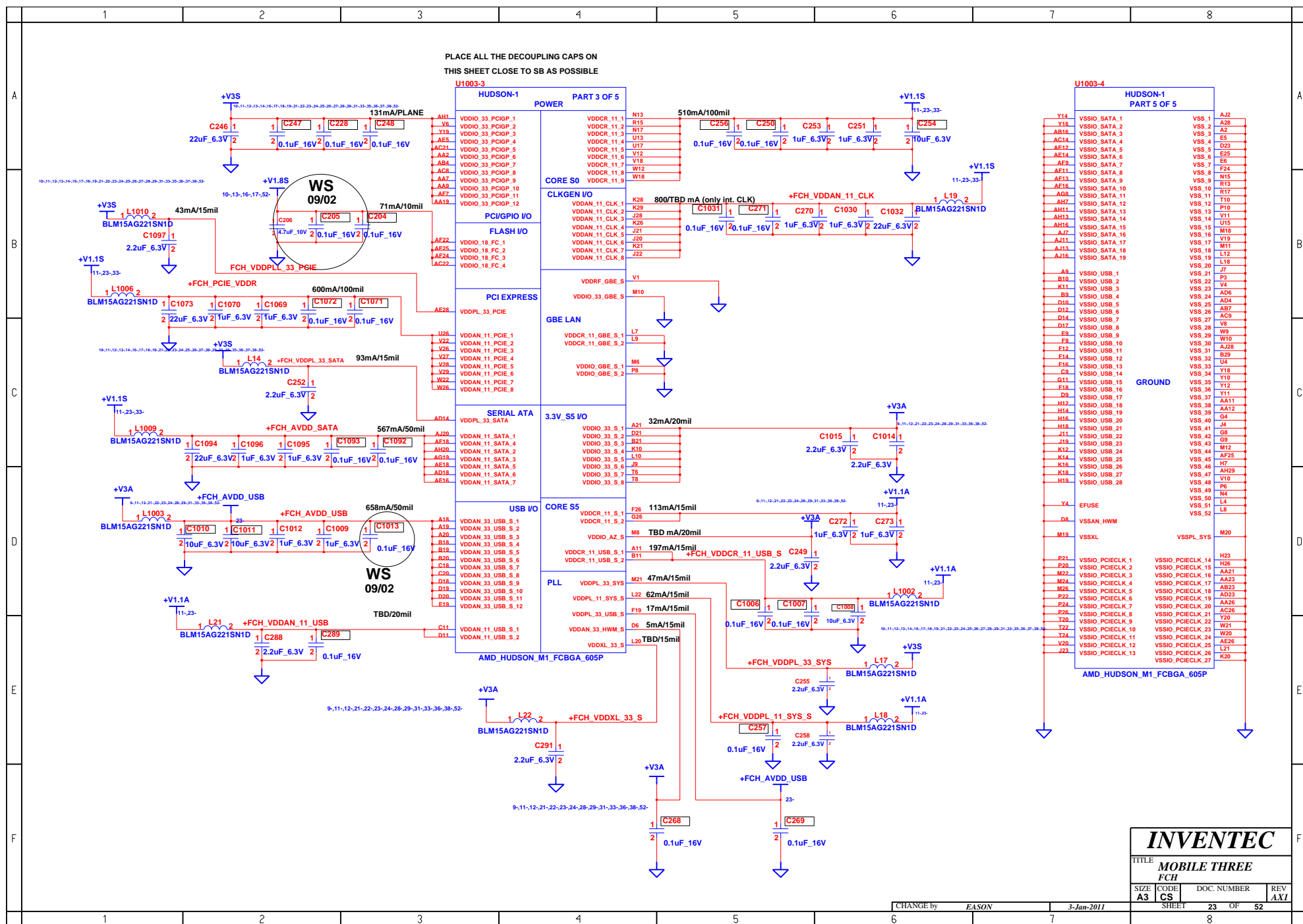
INVENTEC

TITLE			
MOBILE THREE			
DDR3-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		AXI

CHANGE by	EASON	3-Jan-2011	SHEET	19	OF	52
-----------	-------	------------	-------	----	----	----







STRAP OPTION RTC

RTC
P/N: 6027B0039401

STRAP PINS

WS 08/17
AZ97_3S_SDOUT
PCICLK1
PCICLK2
PCICLK3
PCICLK4

WS 09/02
LPCCLK0
LPCCLK1
GPIO200
GPIO199

WS2 11/18
LPCCLK0
LPCCLK1
GPIO200
GPIO199

	AZ97_3S_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPCCLK0	LPCCLK1	GPIO200 EC_PWM3	GPIO199 EC_PWM2
PULL HIGH	Low Power Mode (Not Supported)	Allow PCIe Gen2 (Default)	Watchdog Timer Enabled (Not Supported)	Debug STRAP Enabled	Non-FUSION CLOCK Mode (Default)	Integrated Micro-Controller Enabled (Default)	Internal Clock Generator Enabled (Default)	H _H = Reserved H _L = SPI ROM (Default)	
PULL LOW	Performance Mode (Default)	Force PCIe Gen1	Watchdog Timer Disabled (Default)	Debug STRAP Disabled (Default)	FUSION CLOCK Mode (Default)	Integrated Micro-Controller Disabled	Internal Clock Generator Disabled	L _H = LPC ROM L _L = Reserved	

INVENTEC

TITLE

MOBILE THREE
STRAP OPTION / THERMAL / RTC

SIZE

A3

CODE

CS

DOC. NUMBER

REV

AX1

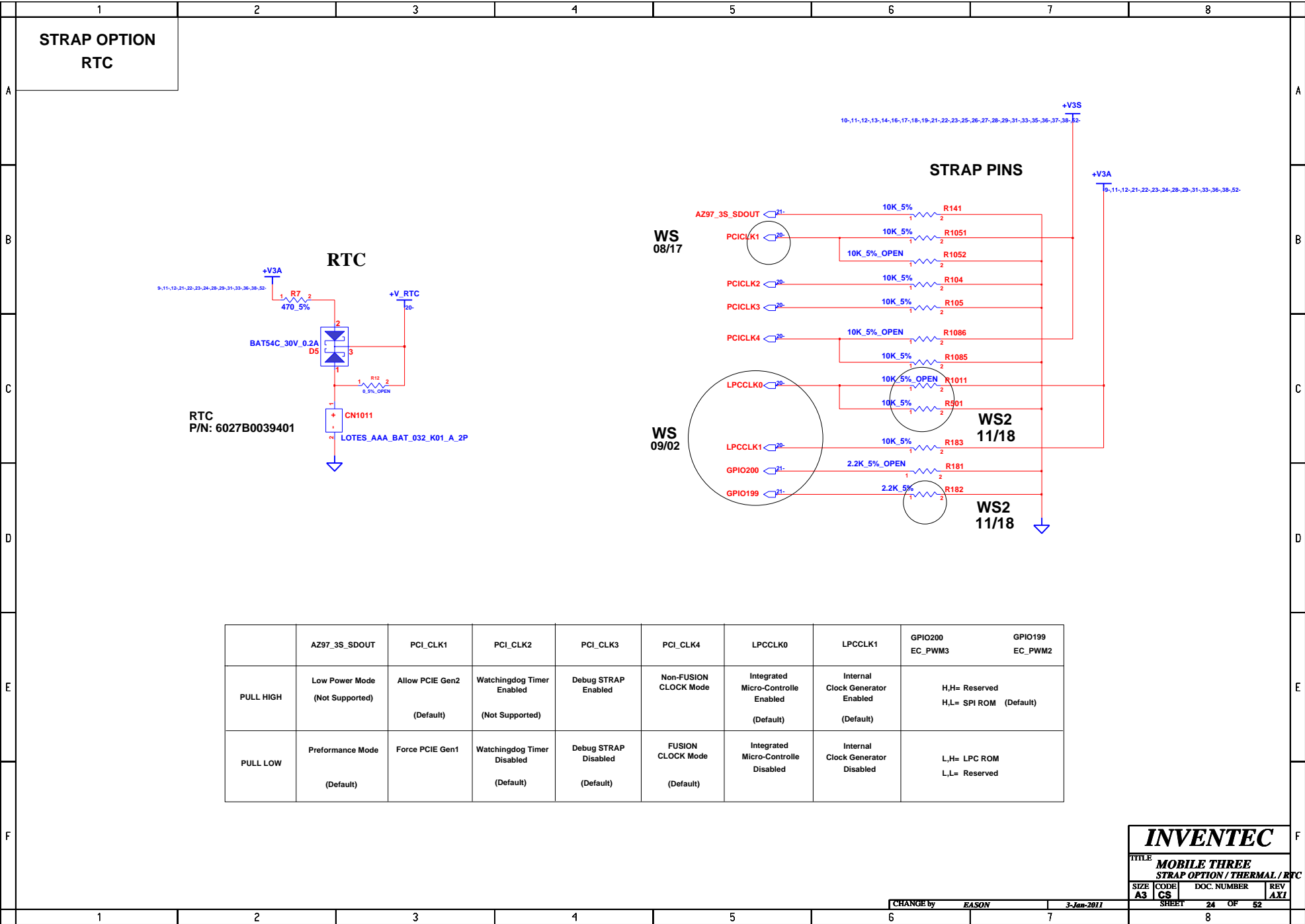
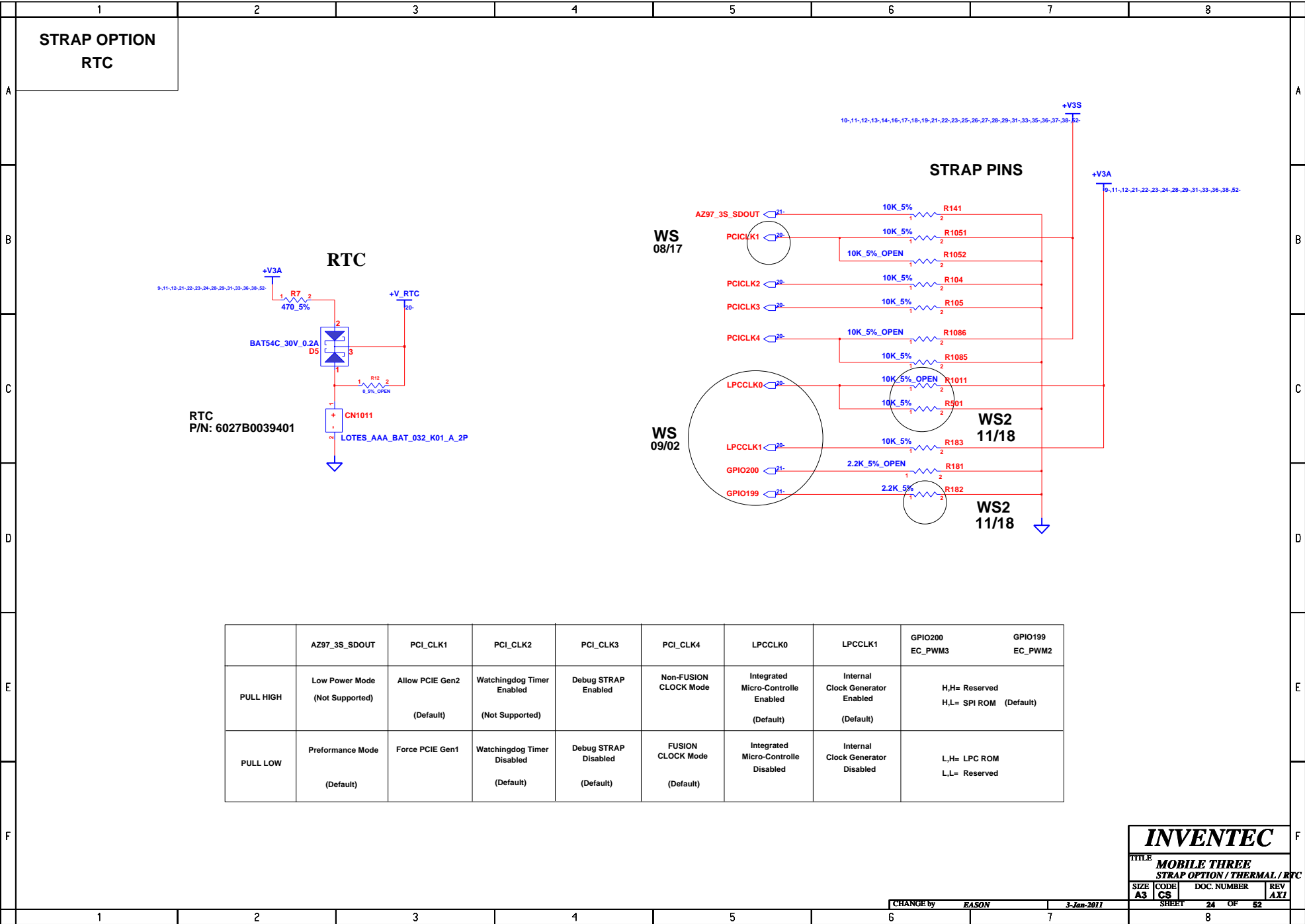
SHEET

24

OF

52

CHANGE by EASON 3-Jan-2011



STRAP OPTION RTC

RTC
P/N: 6027B0039401

STRAP PINS

	AZ97_3S_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPCCLK0	LPCCLK1	GPIO200 EC_PWM3	GPIO199 EC_PWM2
PULL HIGH	Low Power Mode (Not Supported)	Allow PCIe Gen2 (Default)	Watchdog Timer Enabled (Not Supported)	Debug STRAP Enabled	Non-FUSION CLOCK Mode	Integrated Micro-Controller Enabled (Default)	Internal Clock Generator Enabled (Default)	H,H= Reserved H,L= SPI ROM (Default)	
PULL LOW	Performance Mode (Default)	Force PCIe Gen1	Watchdog Timer Disabled (Default)	Debug STRAP Disabled (Default)	FUSION CLOCK Mode (Default)	Integrated Micro-Controller Disabled	Internal Clock Generator Disabled	L,H= LPC ROM L,L= Reserved	

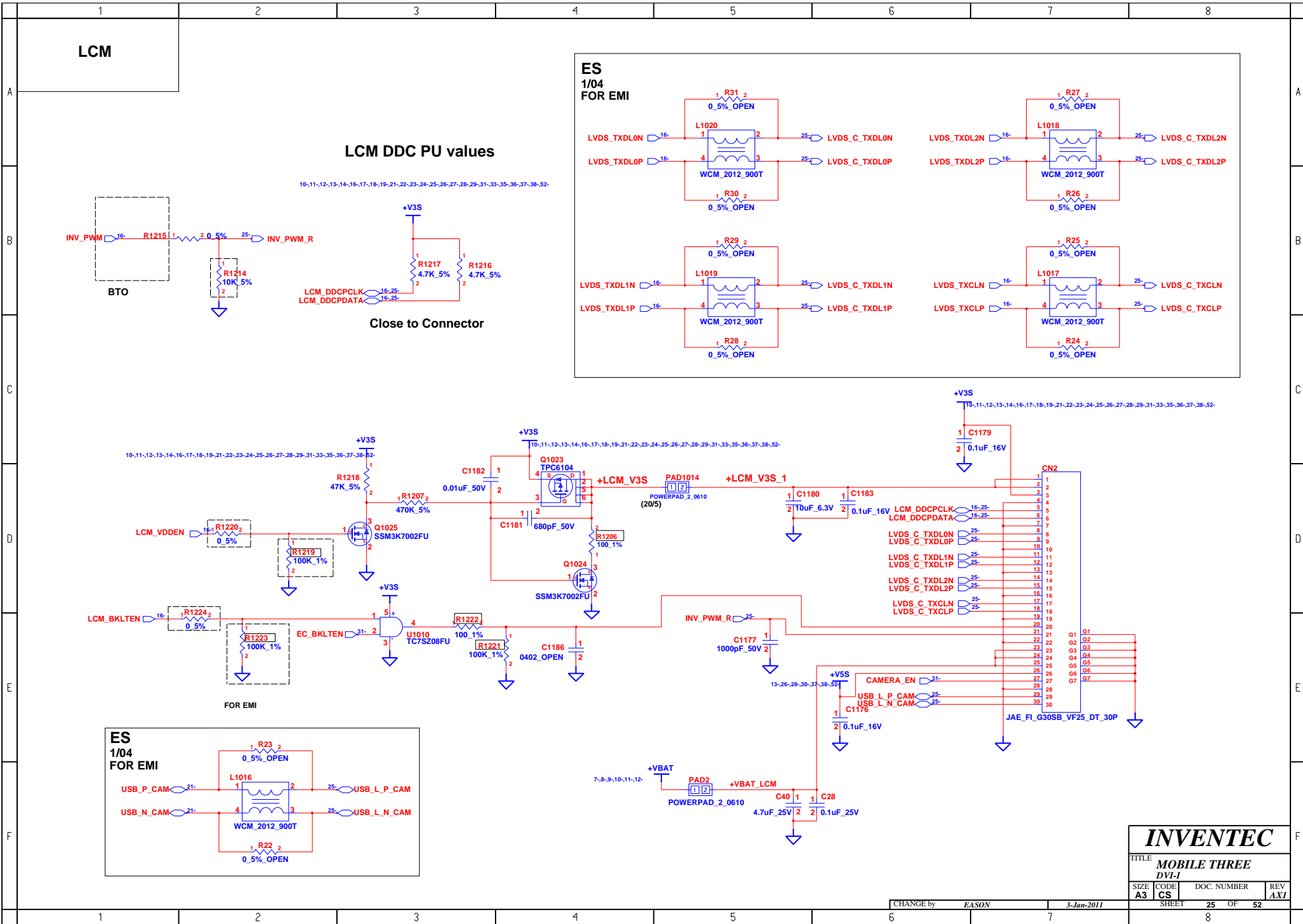
INVENTEC

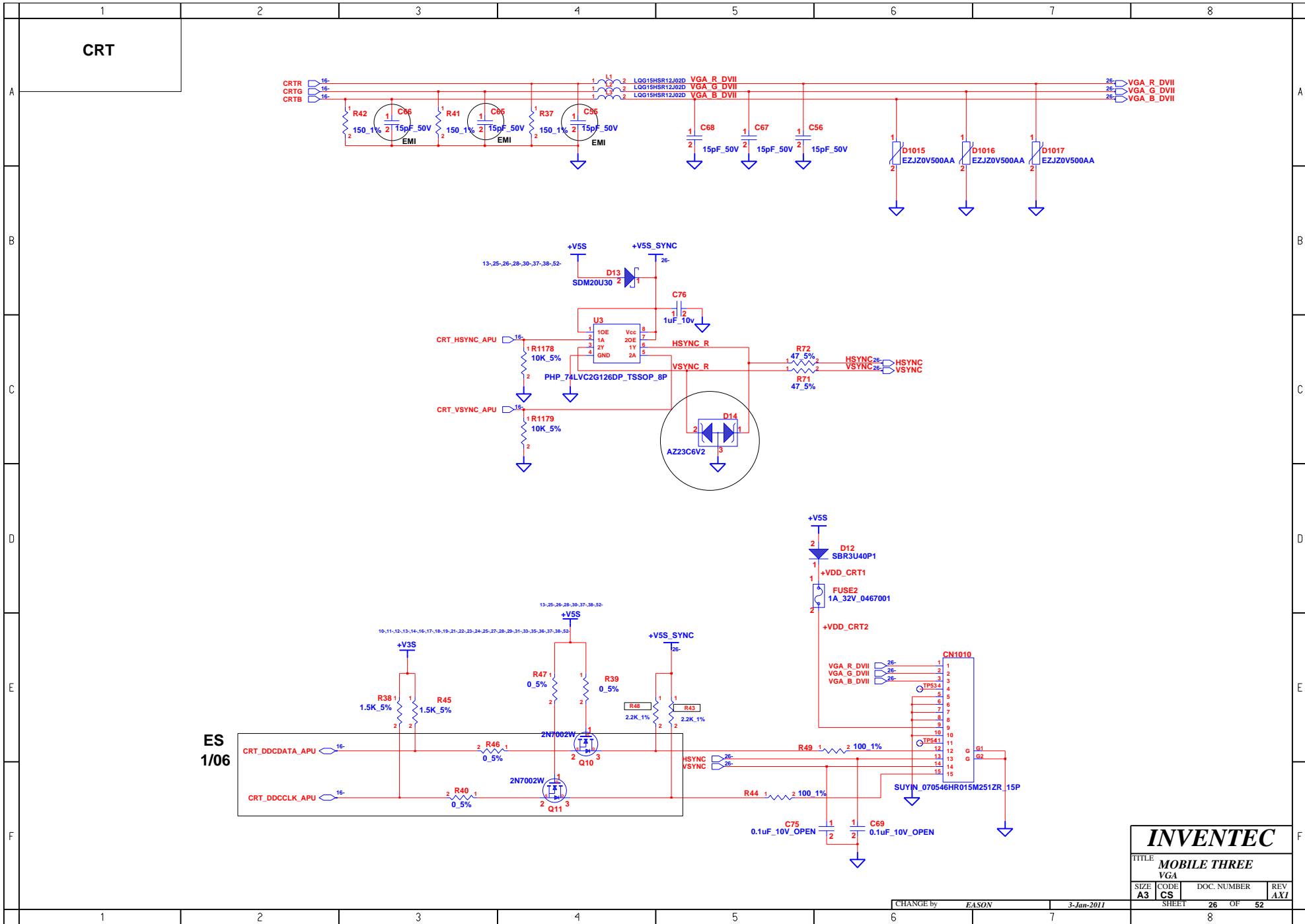
TITLE: MOBILE THREE
STRAP OPTION / THERMAL / RTC

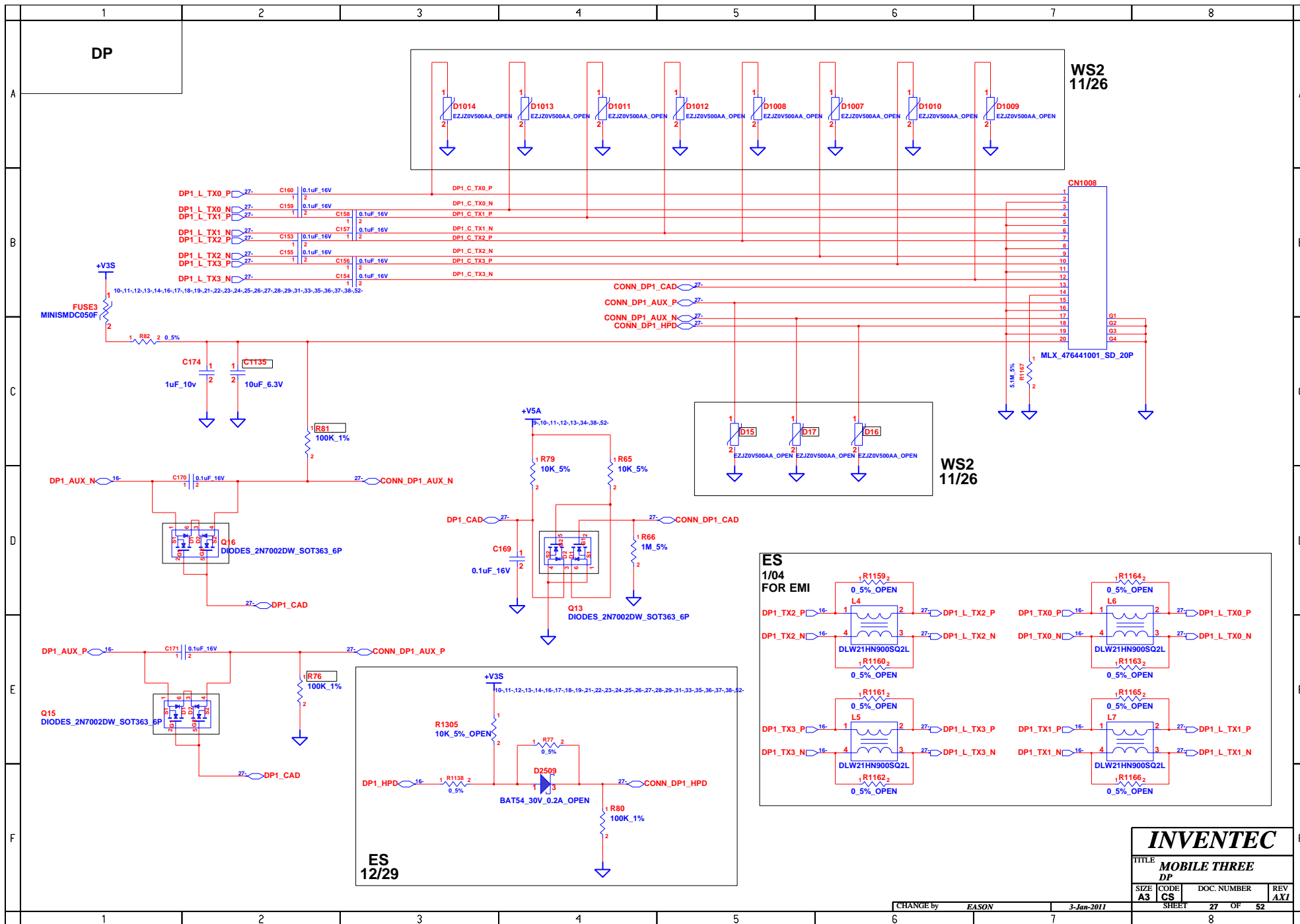
SIZE: A3 CODE: CS DOC. NUMBER: REV: AX1

SHEET: 24 OF 52

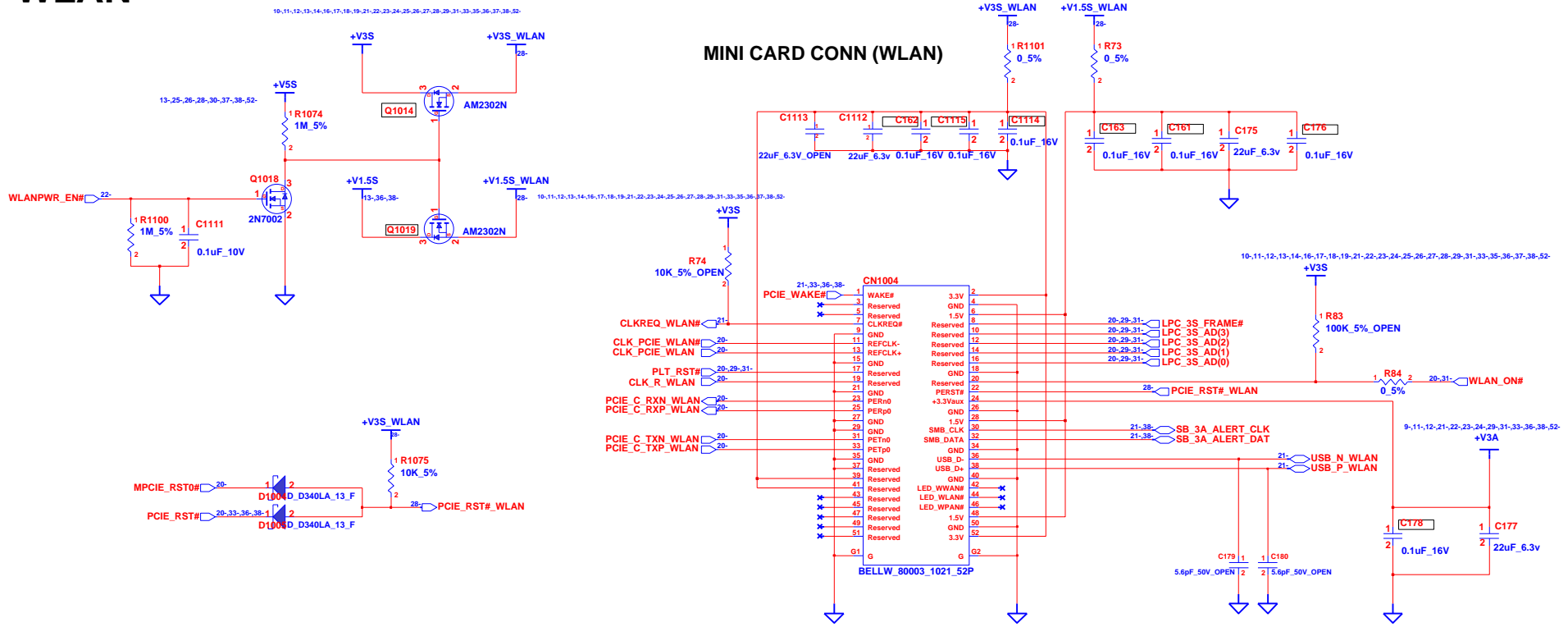
CHANGE by: EASON 3-Jan-2011



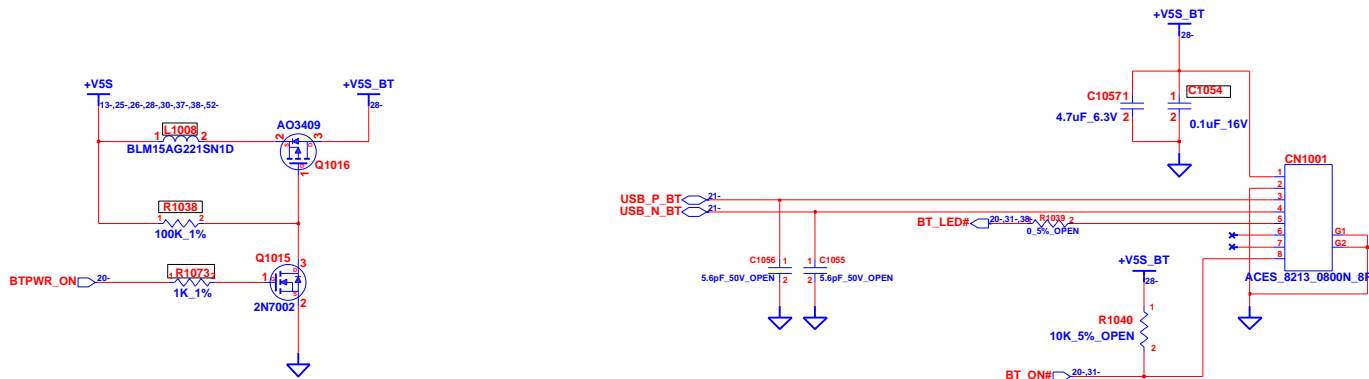




WLAN



BT

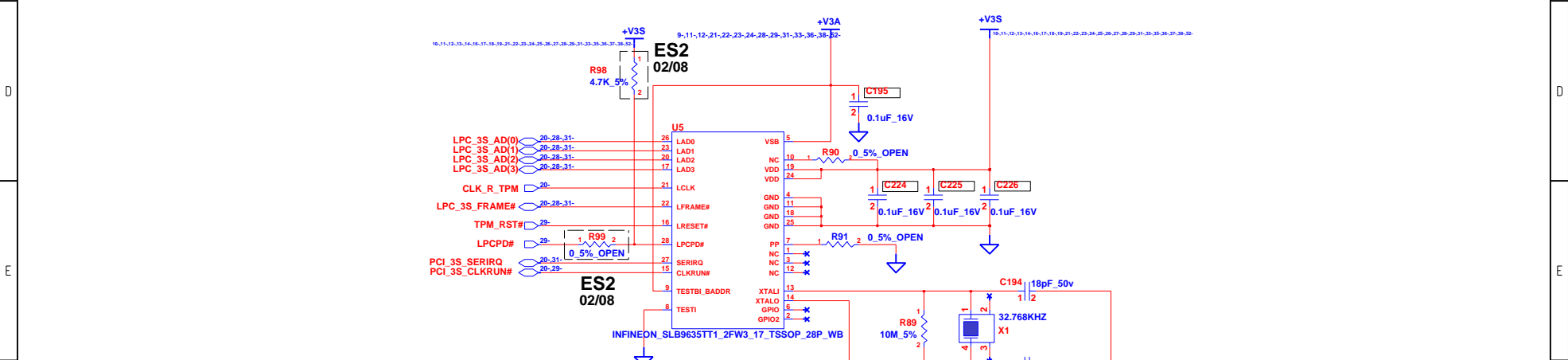
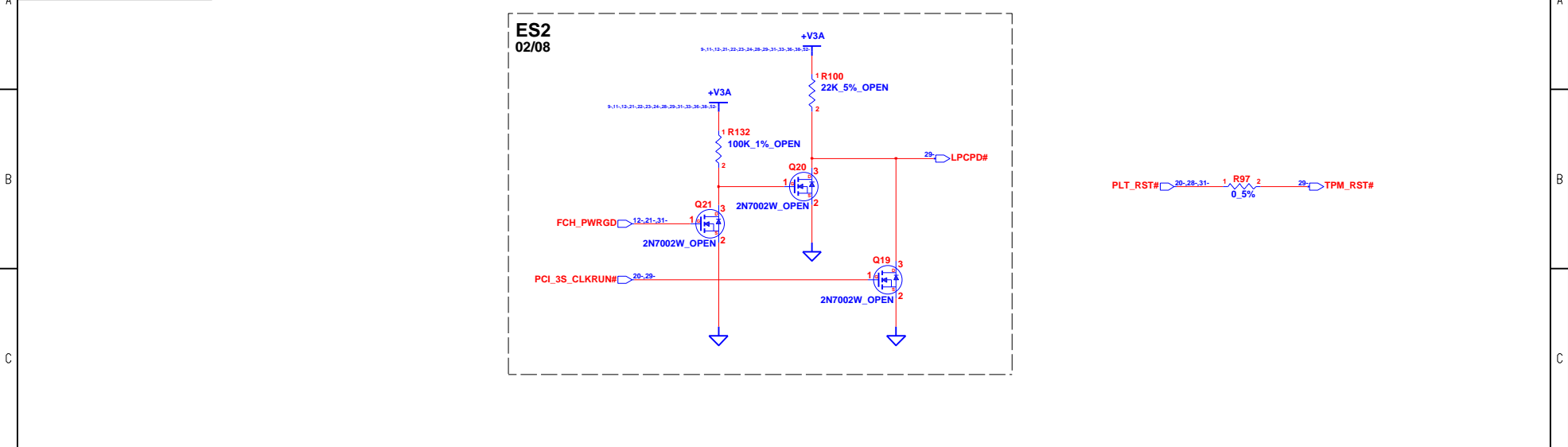


INVENTEC

TITLE			
MOBILE THREE			
WLAN/BT			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		AXI
SHEET		28	OF 52

CHANGE by EASON 3-Jan-2011

	1	2	3	4	5	6	7	8	
A	TPM								

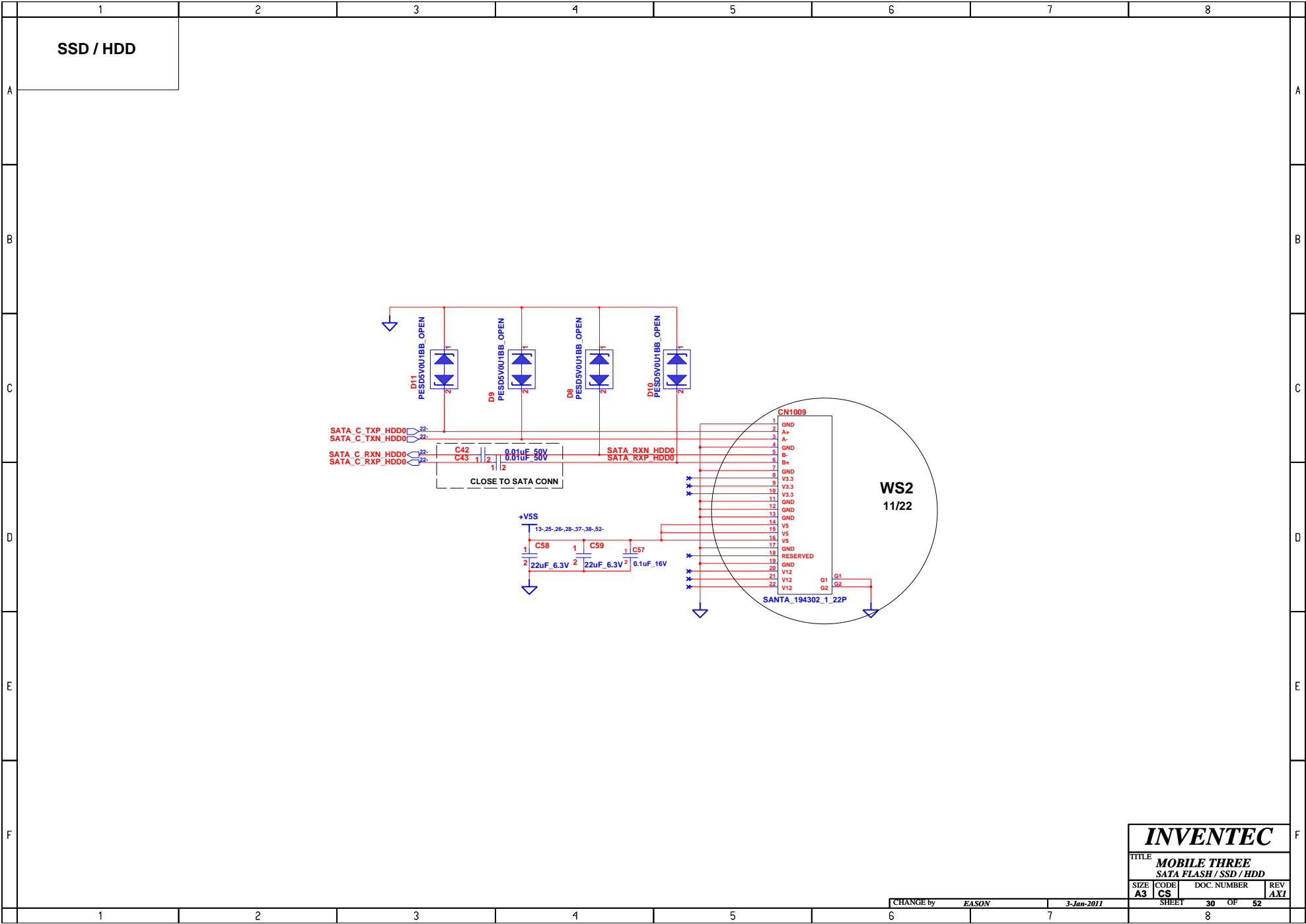


LPC_PWRDWN_L is used to force LPC peripherals to go into sleep mode
Device may require that this be asserted before enteign S3
Device may require that this be asserted before stoping this 33MHz clock

Diagram illustrating the signal trace for LPC_PWRDWN_L. The signal is shown as a red line with a blue arrow pointing to it. The signal is labeled 'C193 1' and '2 18pF_50v'. The signal is connected to a component labeled 'LPC_PWRDWN_L'.

TITLE	MOBILE THREE TPM
-------	-----------------------------------

1	2	3	4	5	6	7	8	
---	---	---	---	---	---	---	---	--



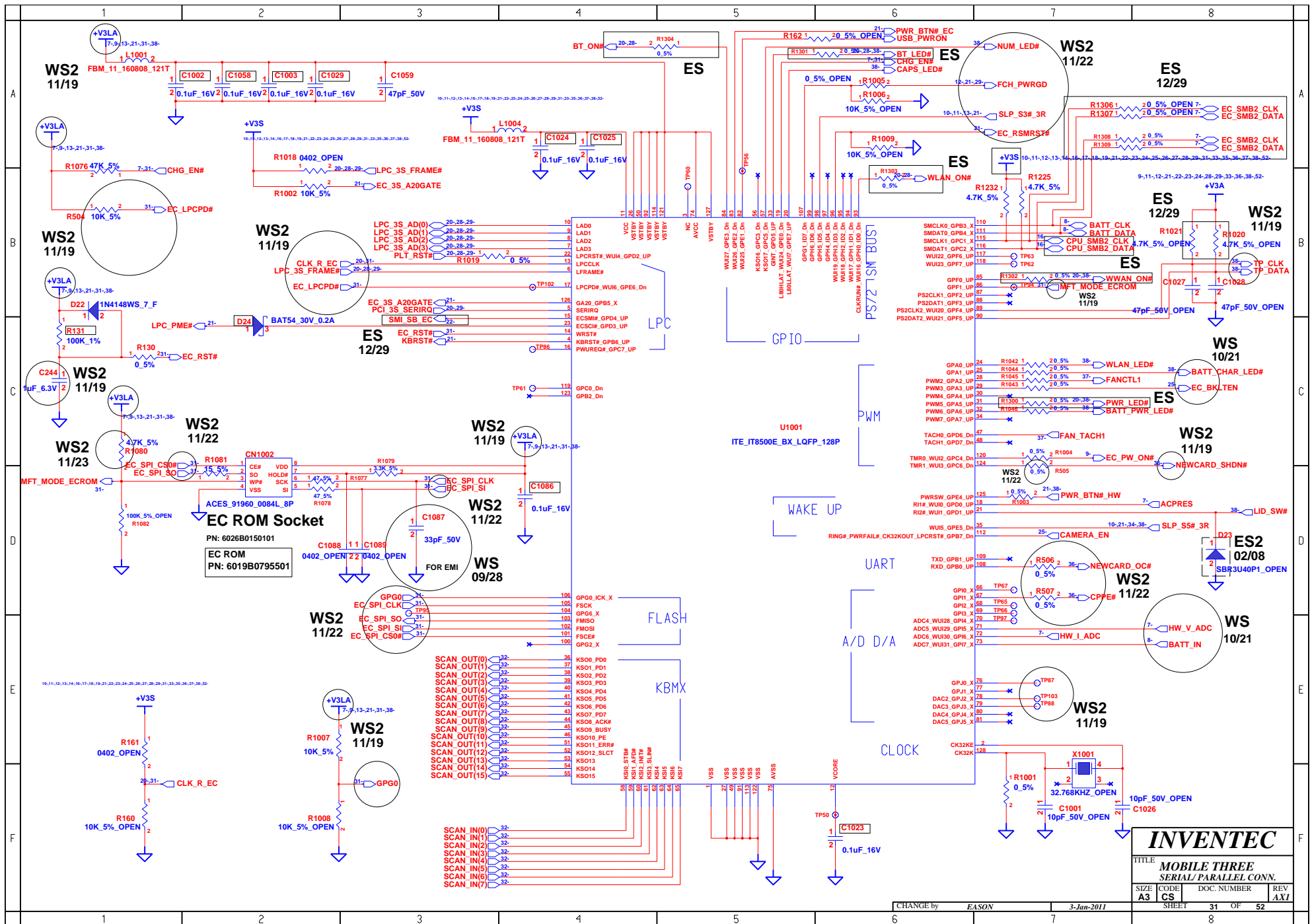
INVENTEC

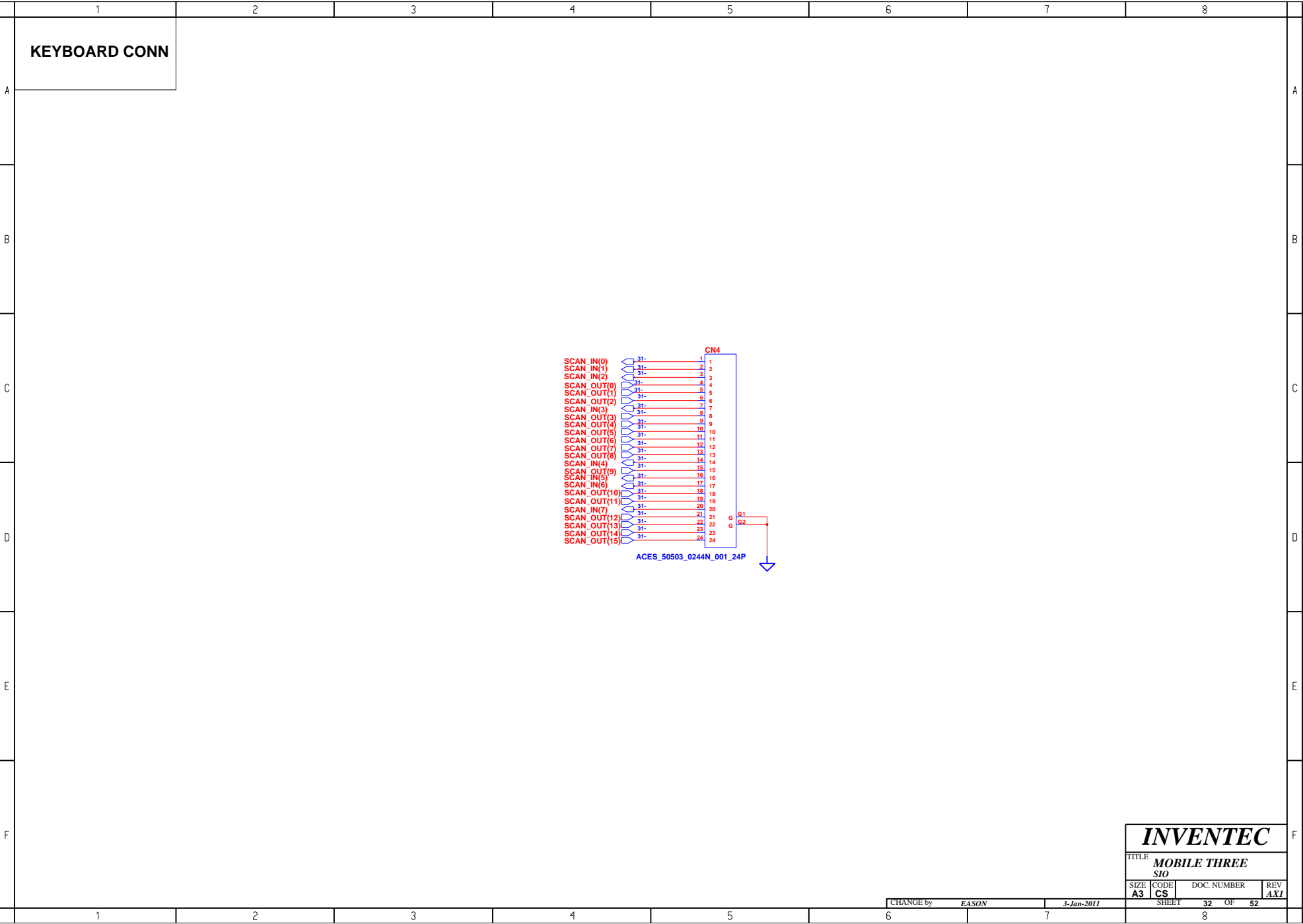
TITLE
**MOBILE THREE
SATA FLASH / SSD / HDD**

SIZE A3	CODE CS	DOC. NUMBER	REV AXI
-------------------	-------------------	-------------	-------------------

CHANGE by **EASON** 3-Jan-2011

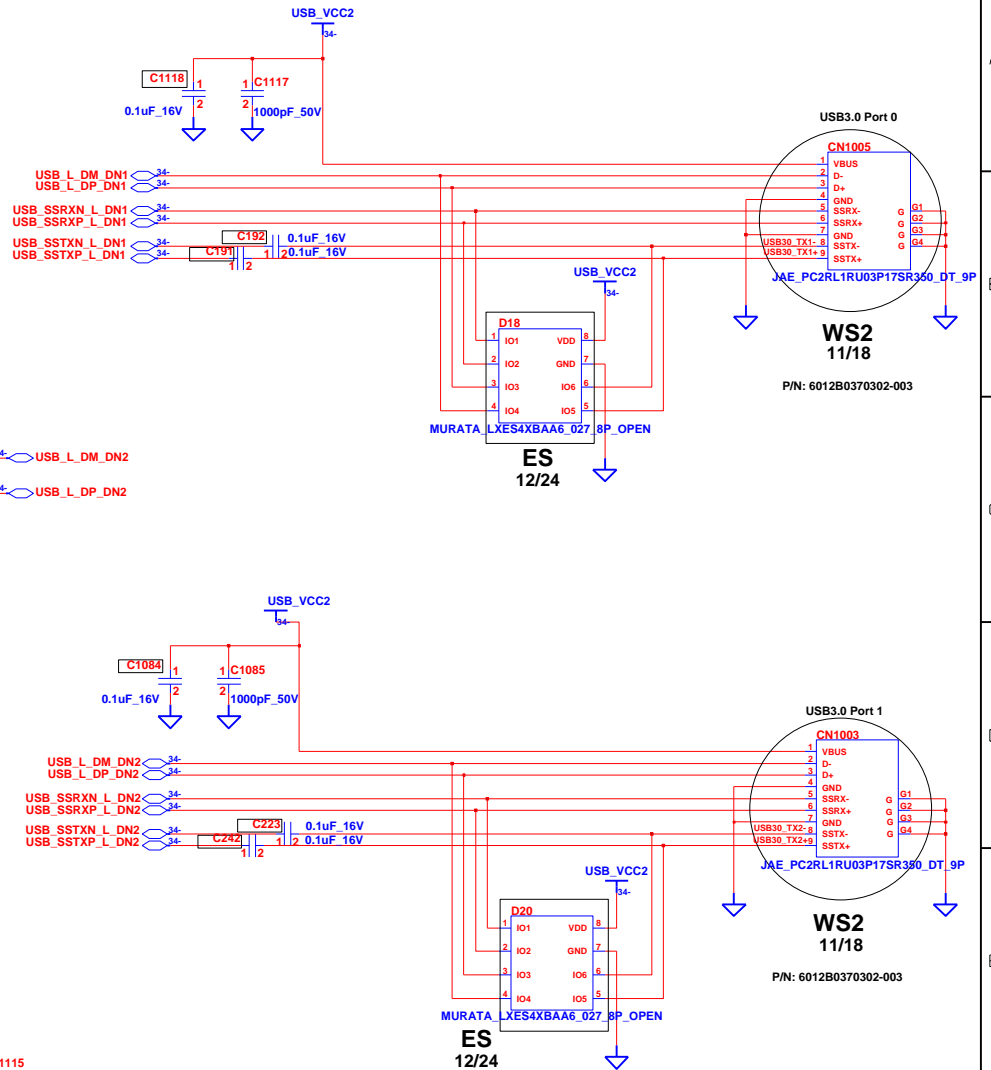
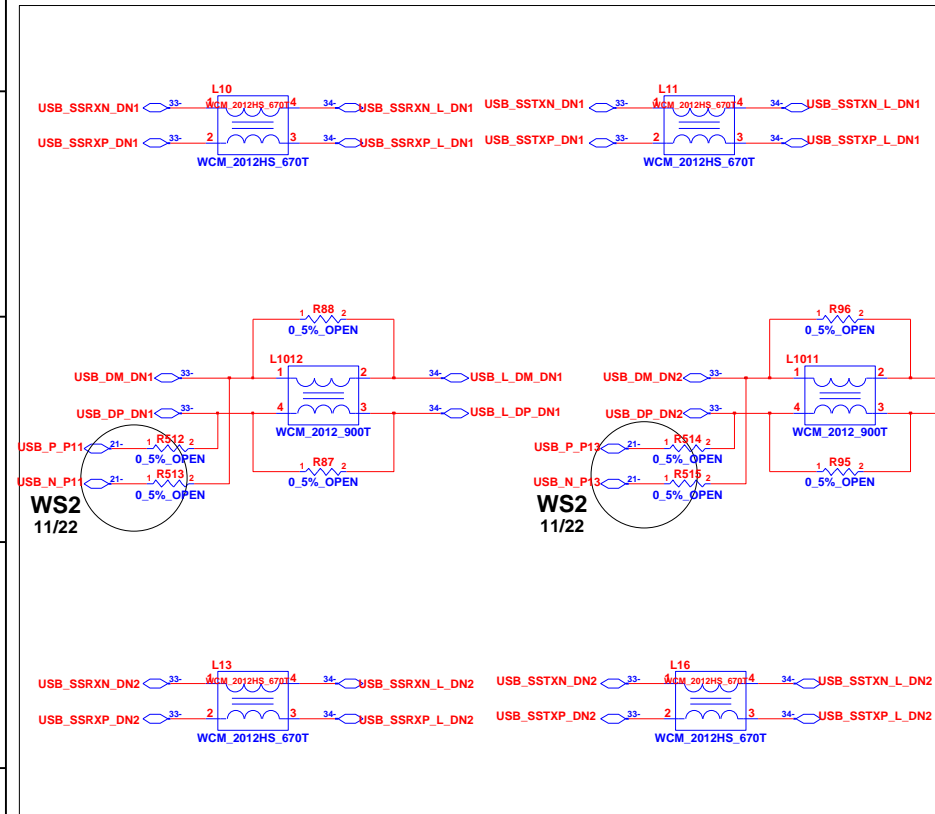
SHEET **30** OF **52**





DUAL USB 3.0 CONN

ES
1/04
FOR EMI

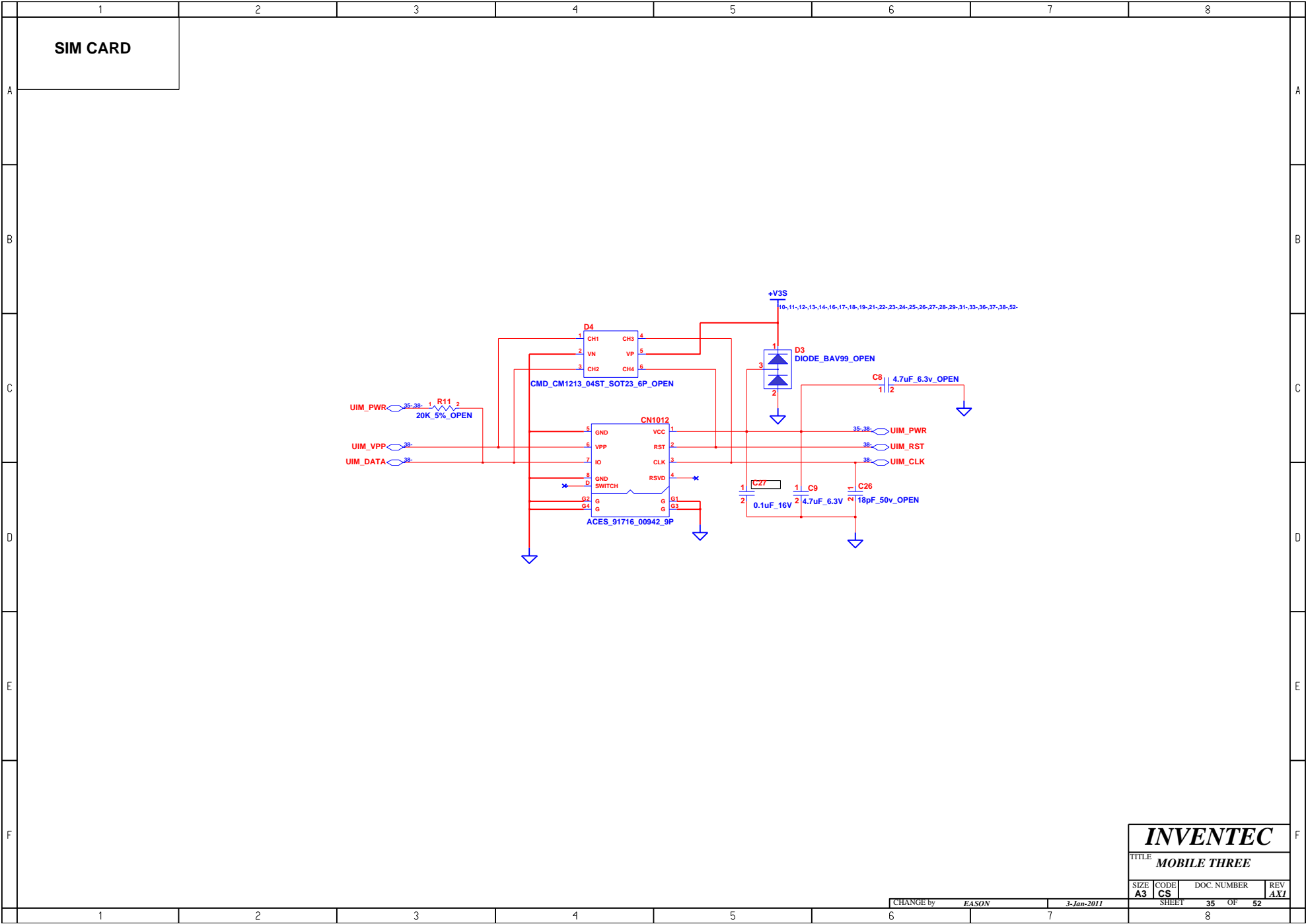


INVENTEC

TITLE			
<i>MOBILE THREE</i>			
<i>DUAL USB3.0 CONN./ USB HEADER</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		AX1

CHANGE by	<i>EASON</i>	<i>3-Jan-2011</i>
-----------	--------------	-------------------

A3	CS		AX1
SHEET		34	OF 52
		8	



INVENTEC

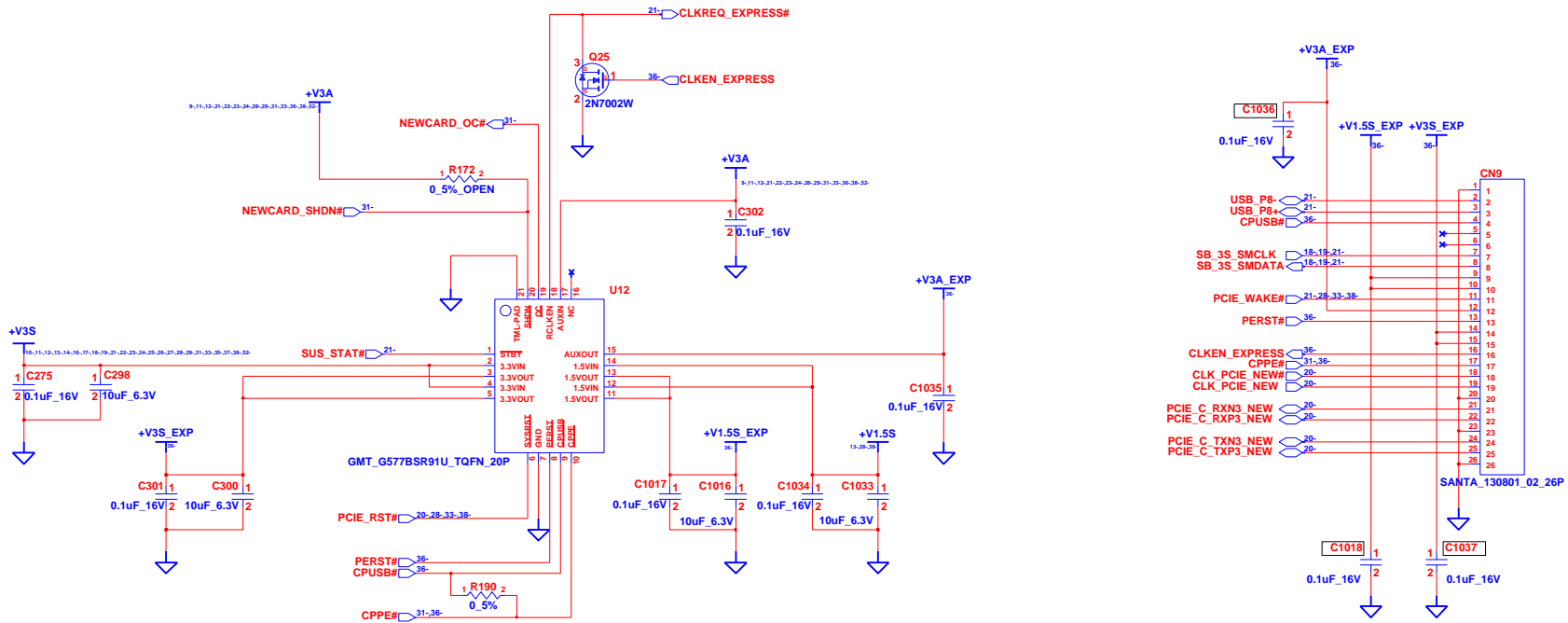
TITLE **MOBILE THREE**

SIZE	CODE	DOC. NUMBER	REV
A3	CS		AXI

CHANGE by EASON 3-Jan-2011

SHEET 35 OF 52

EXPRESS CARD

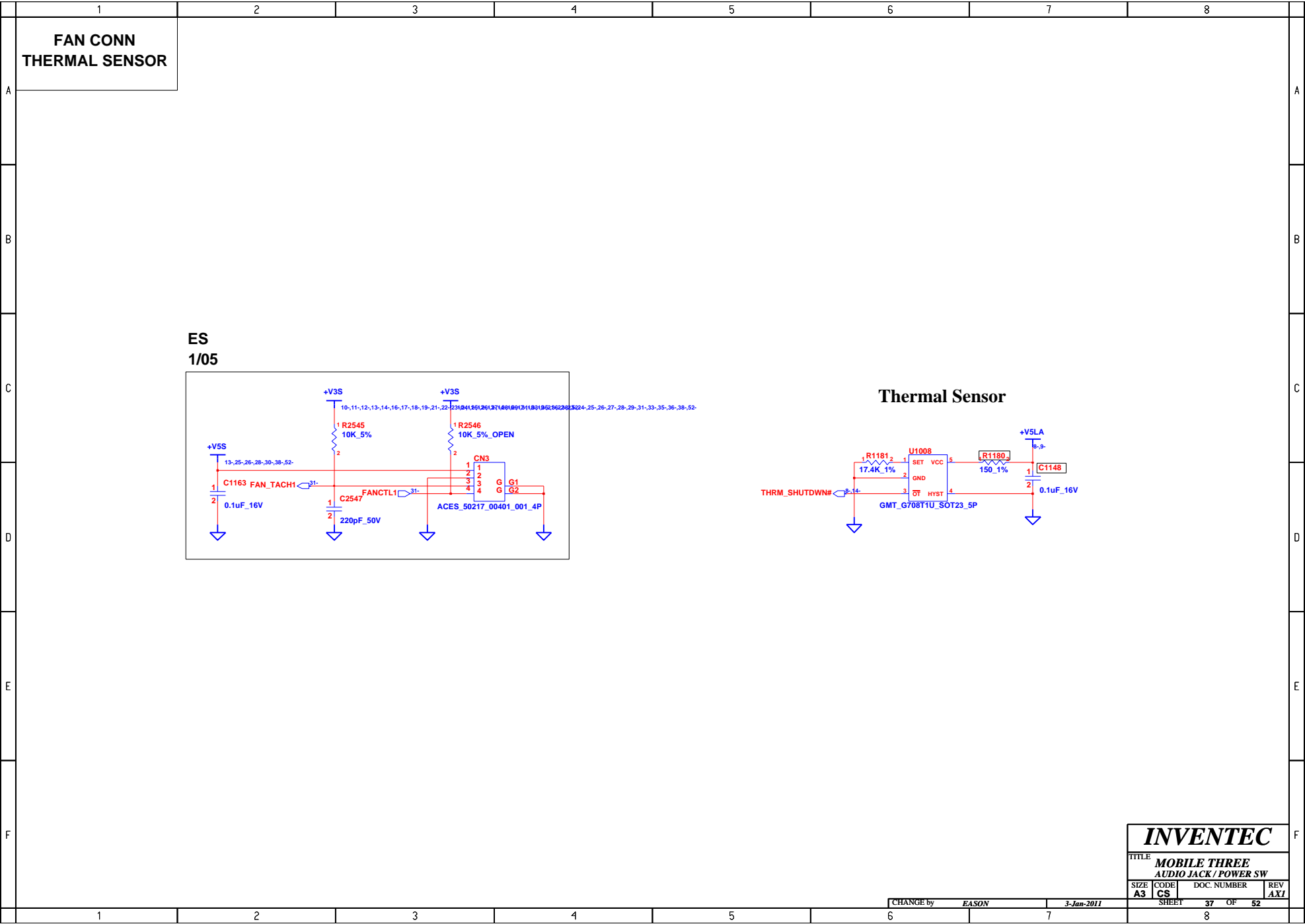


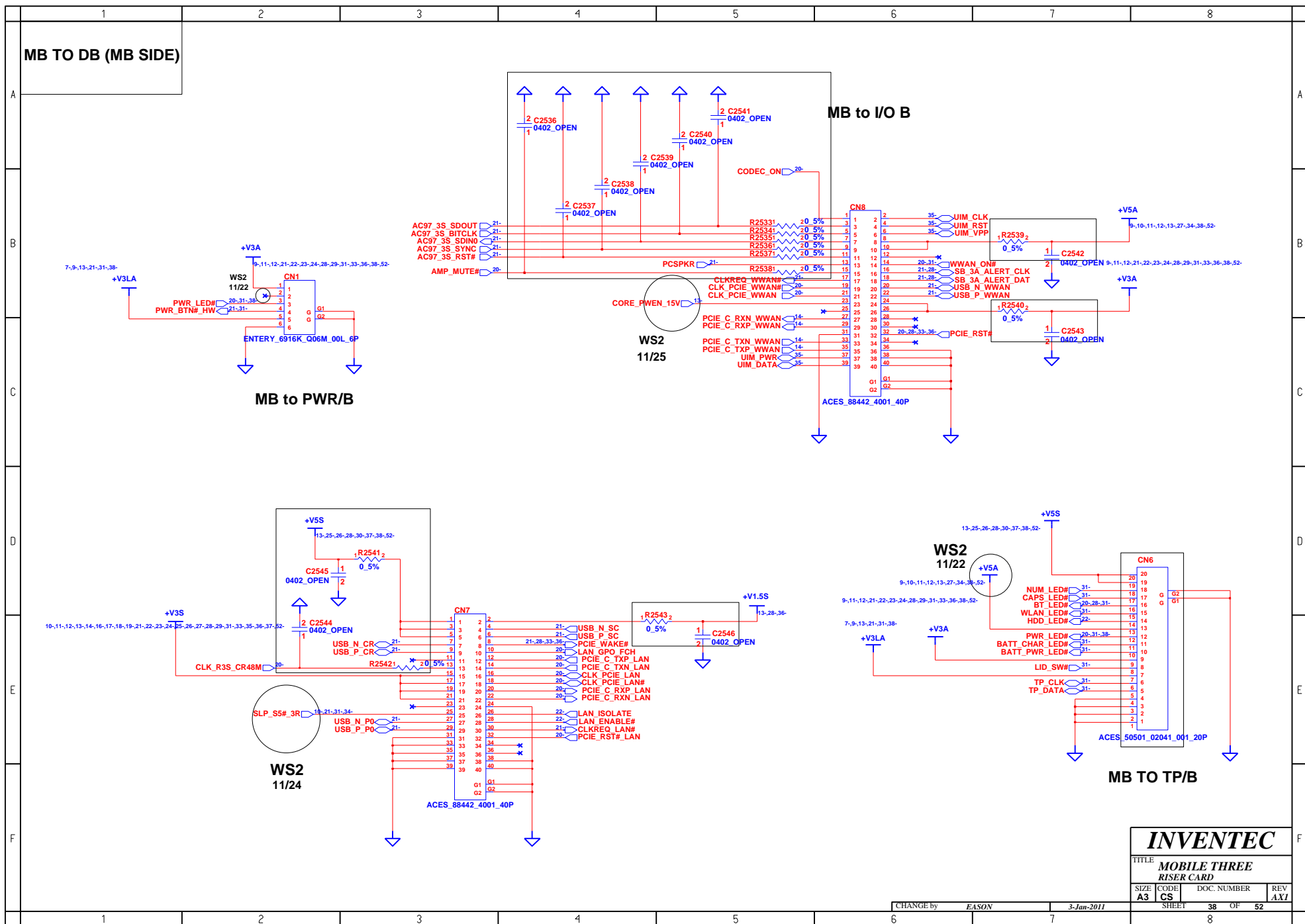
INVENTEC

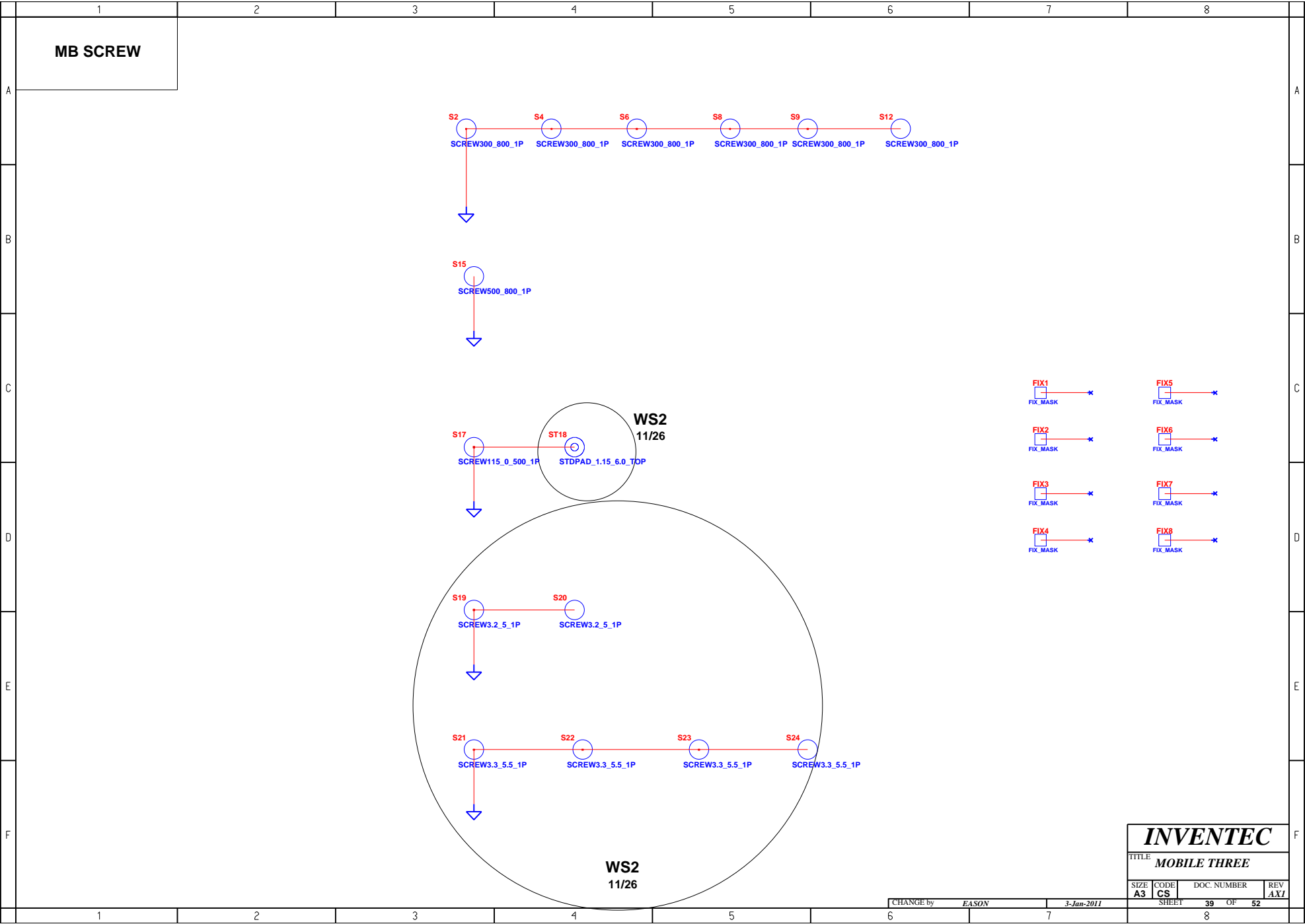
TITLE
**MOBILE THREE
USB CONN**

SIZE A3	CODE CS	DOC. NUMBER SHEET 36 OF 52	REV AXI
-------------------	-------------------	--------------------------------------	-------------------

CHANGE by **EASON** 3-Jan-2011







INVENTEC

TITLE **MOBILE THREE**

SIZE A3	CODE CS	DOC. NUMBER	REV AXI
-------------------	-------------------	-------------	-------------------

CHANGE by **EASON** 3-Jan-2011

SHEET 39 OF 52

[illegible]

CODEC

Locate under CODEC
use 80 mills wide trace
bridging AGND and DGND planes

WS 10/20

U2003
REA_ALC268_VB1_GR_ACER_LQFP_48P

U2002
AM2321P

Q2003
2N7002

Place close to Codec

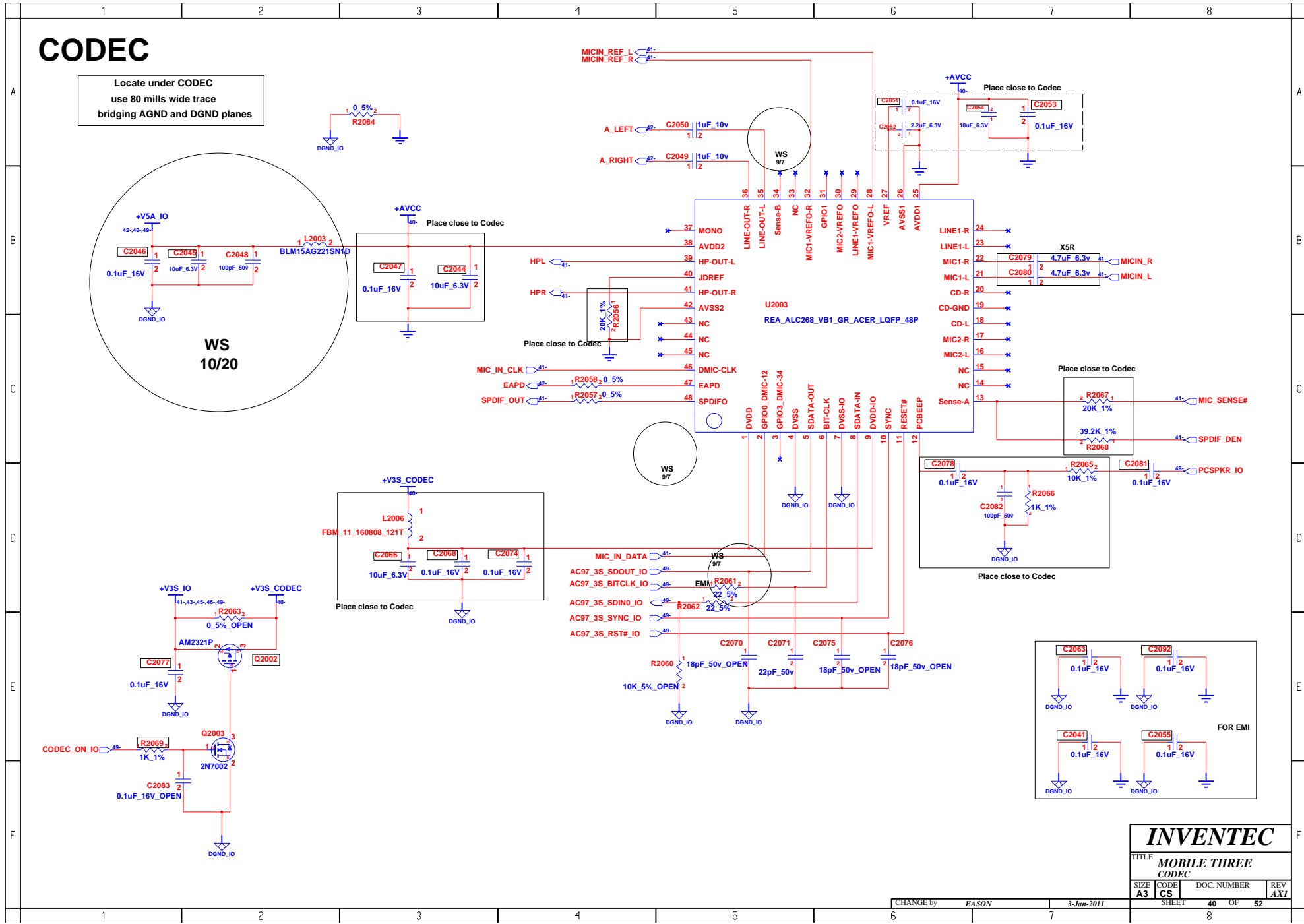
FOR EMI

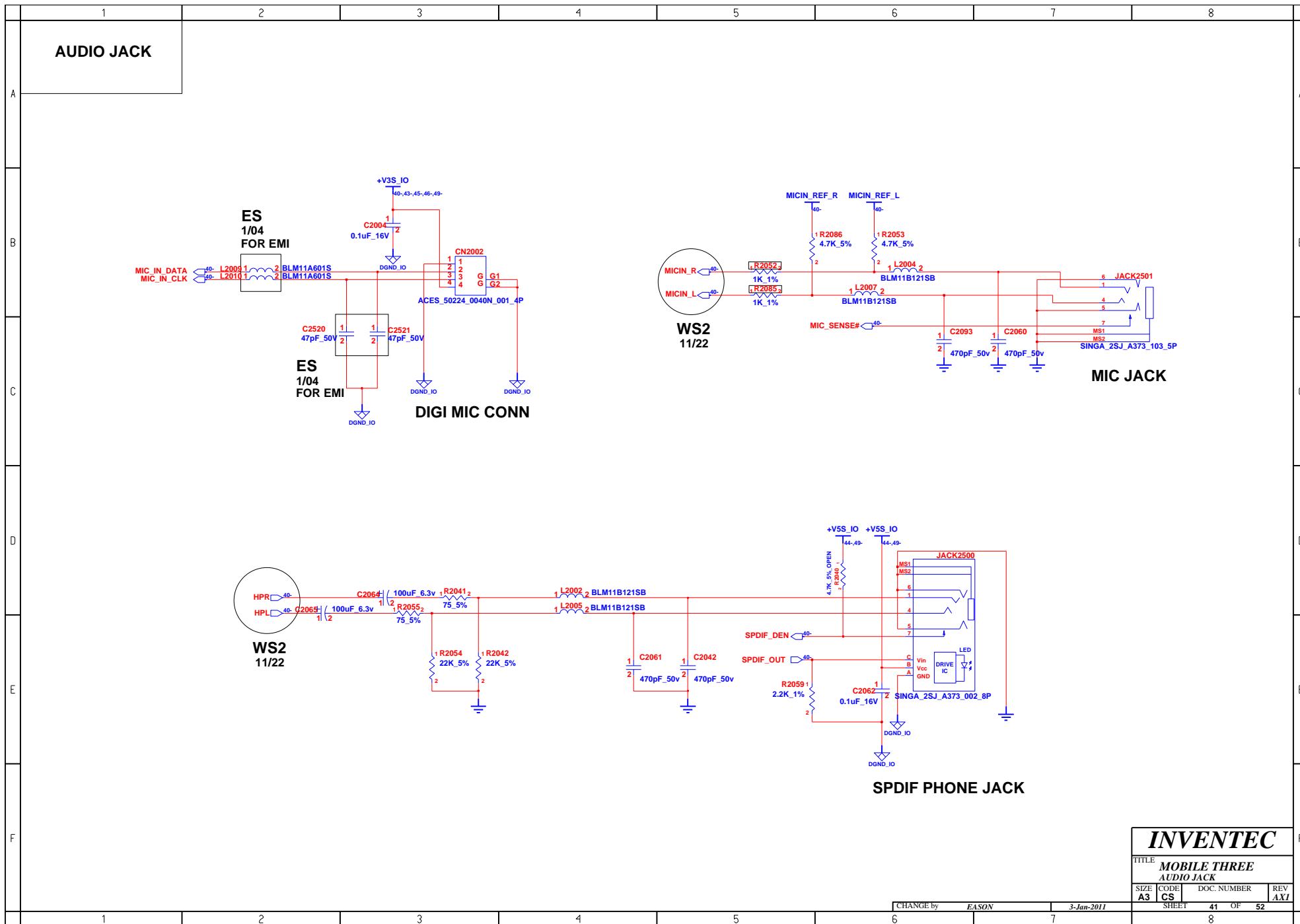
INVENTEC
MOBILE THREE
CODEC

SIZE	CODE	DOC. NUMBER	REV
A3	CS	3-Jun-2011	AX1

CHANGE by: EASON 3-Jun-2011

SHEET 40 OF 52

[illegible][illegible]



AMP / SPEAKER

EAPD when output high, turn on OP
EAPD when output low, turn off OP

WS 10/27

AMP

ES 12/24

INT. SPEAKER

WS 10/29

INVENTEC

TITLE MOBILE THREE AMP/SPEAKER

SIZE A3 CODE CS DOC. NUMBER REV AXI

SHEET 8 OF 52

CHANGE by EASON 3-Jan-2011

AMP / SPEAKER

EAPD when output high, turn on OP
EAPD when output low, turn off OP

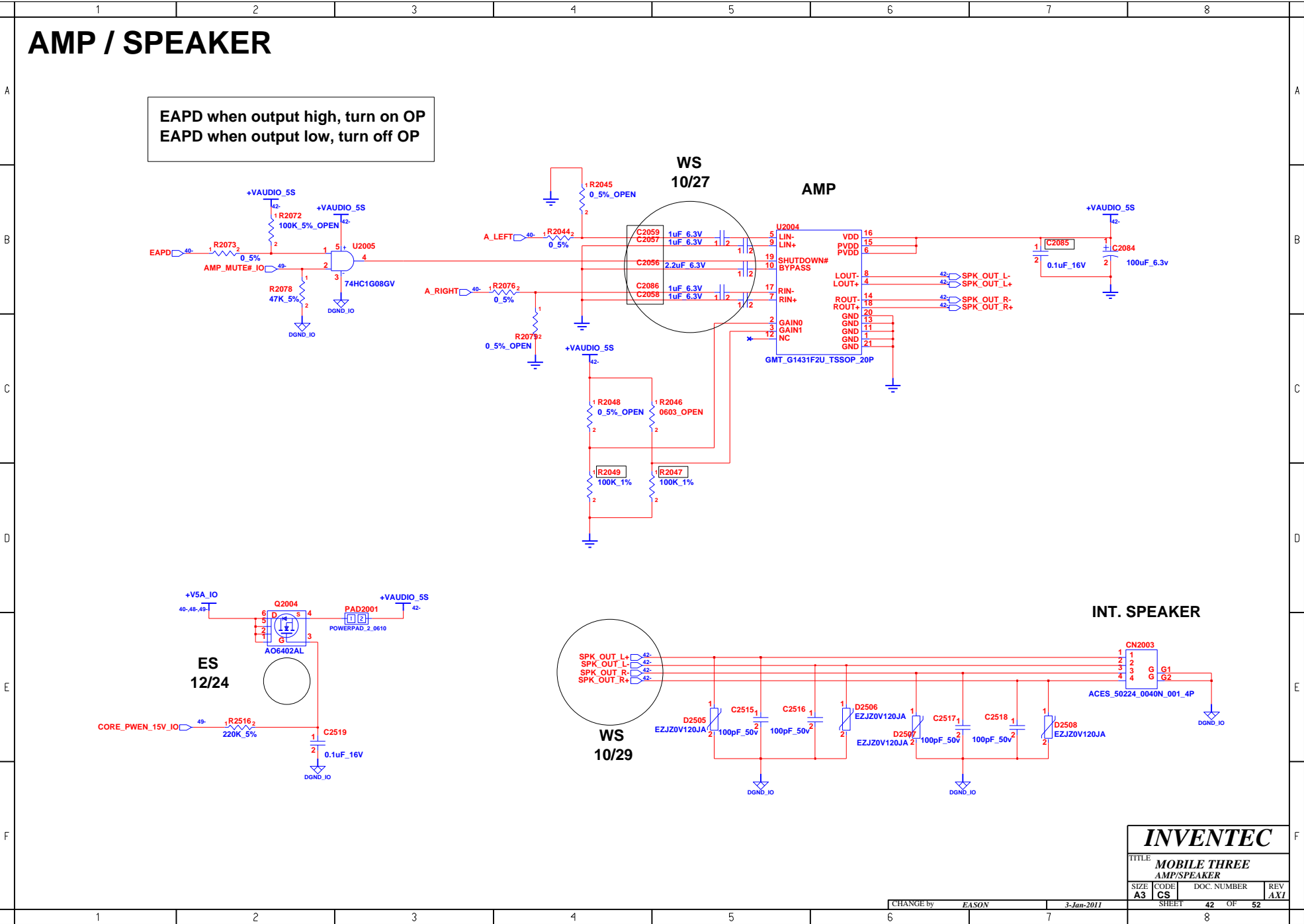
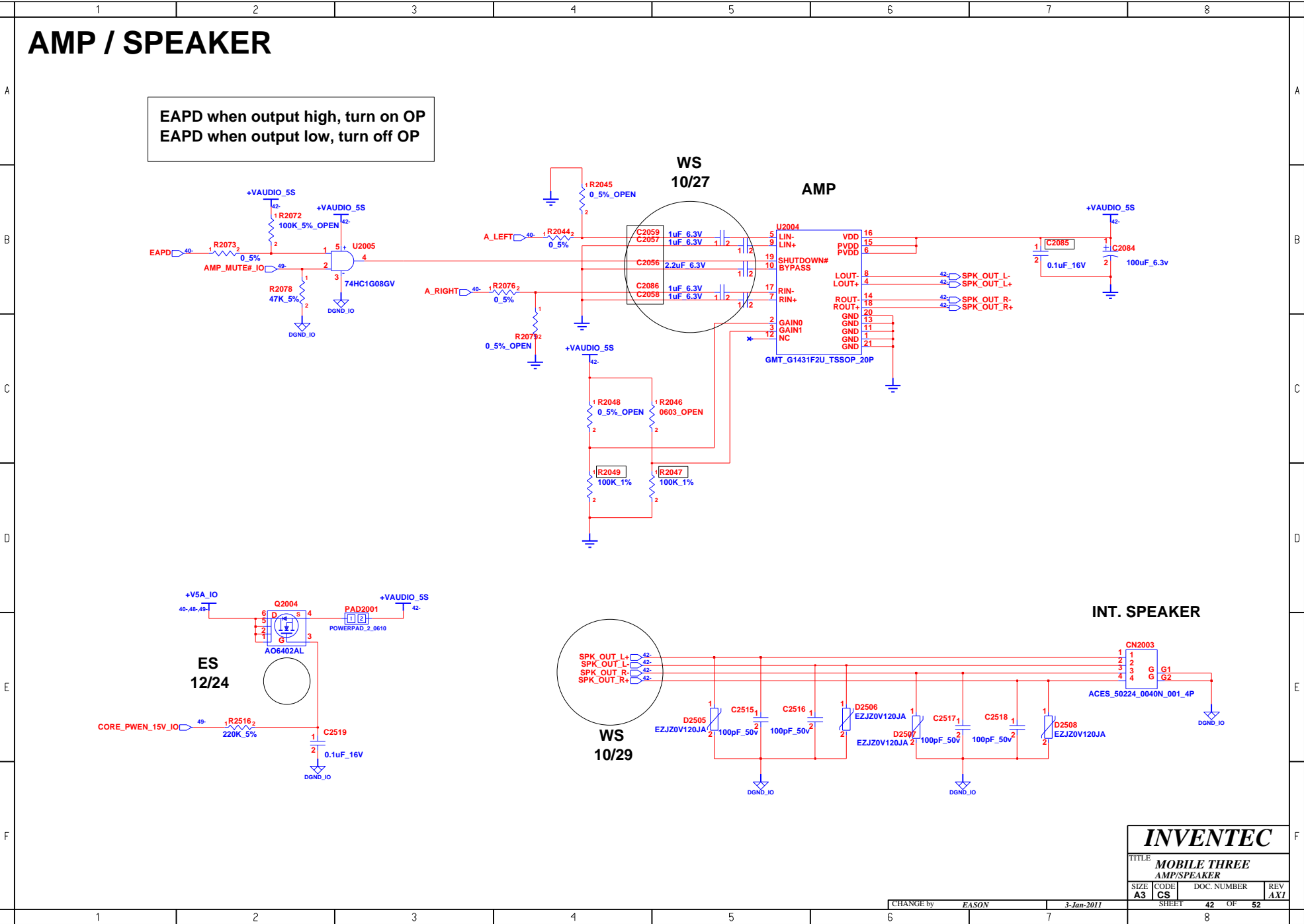
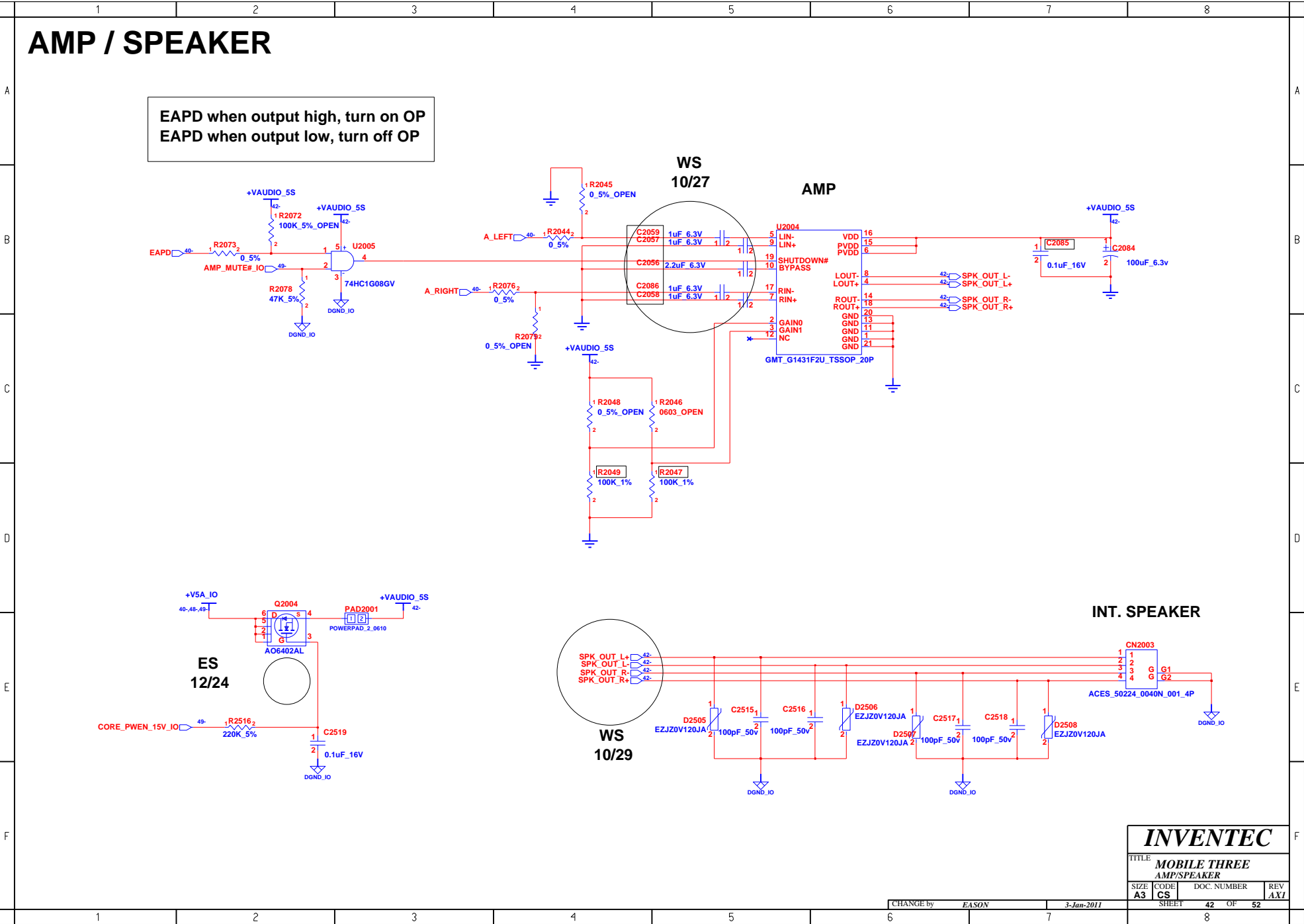
INT. SPEAKER

INVENTEC

TITLE
MOBILE THREE
AMP/SPEAKER

SIZE CODE DOC. NUMBER REV
A3 CS 3-Jan-2011 AX1

CHANGE by EASON 8 OF 52

[illegible]

AMP / SPEAKER

EAPD when output high, turn on OP
EAPD when output low, turn off OP

INT. SPEAKER

INVENTEC

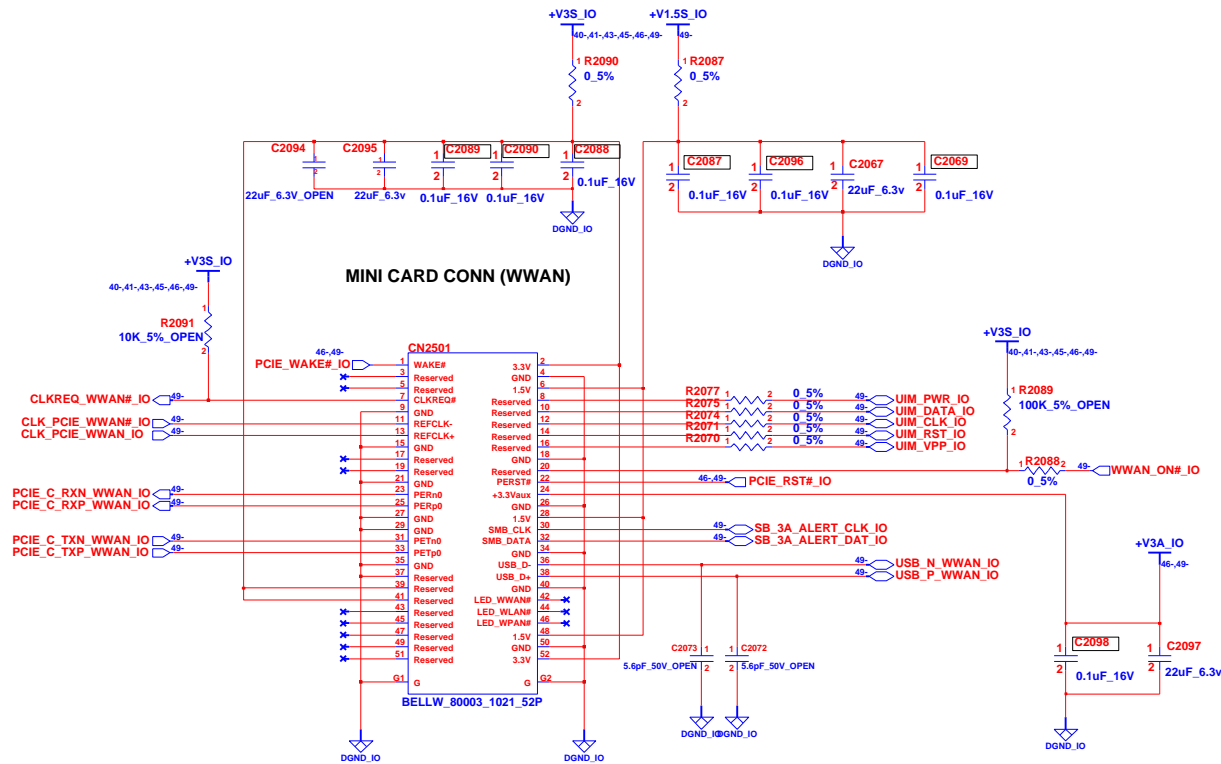
TITLE
MOBILE THREE
AMP/SPEAKER

SIZE CODE DOC. NUMBER REV
A3 CS

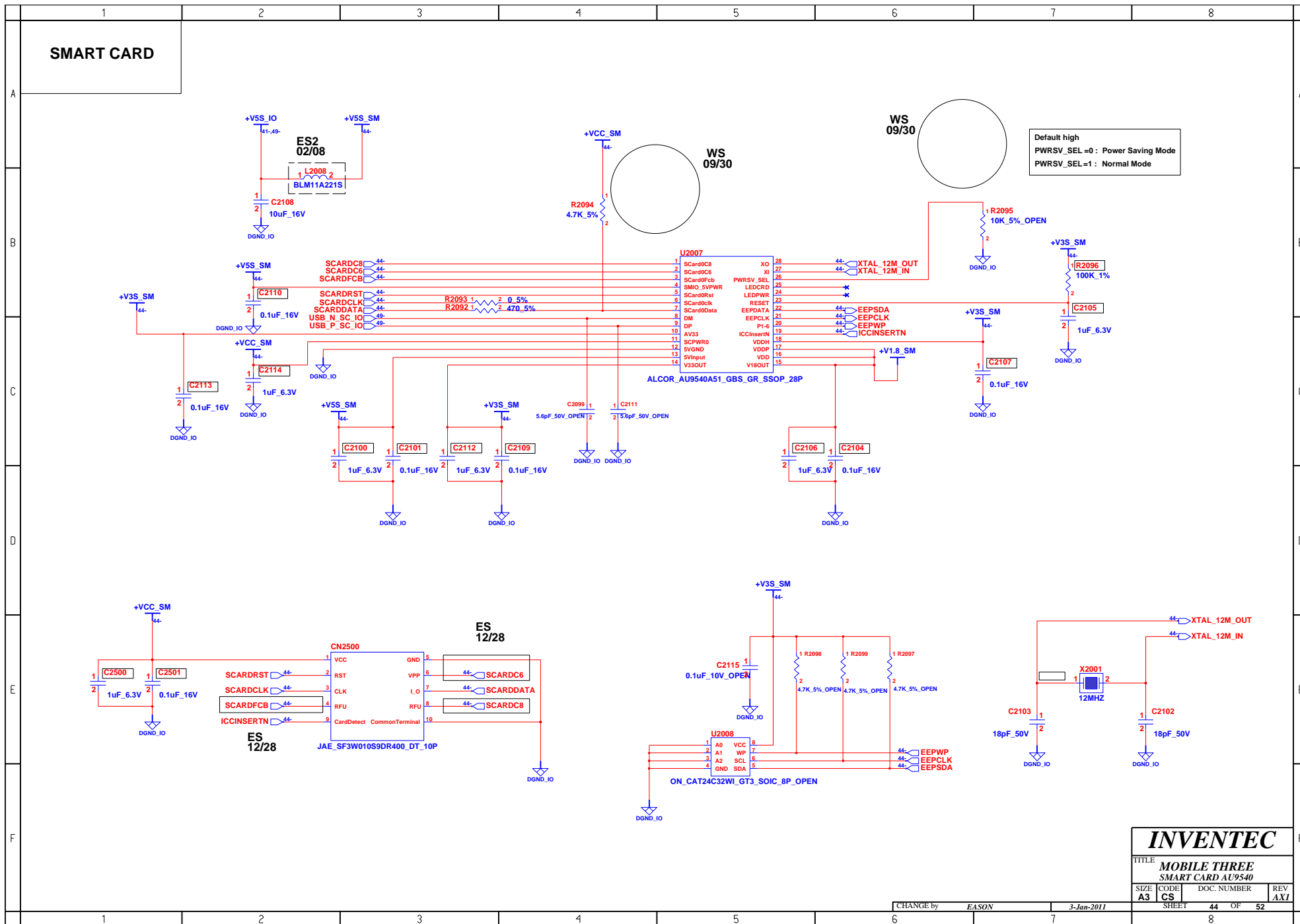
SHEET 42 OF 52

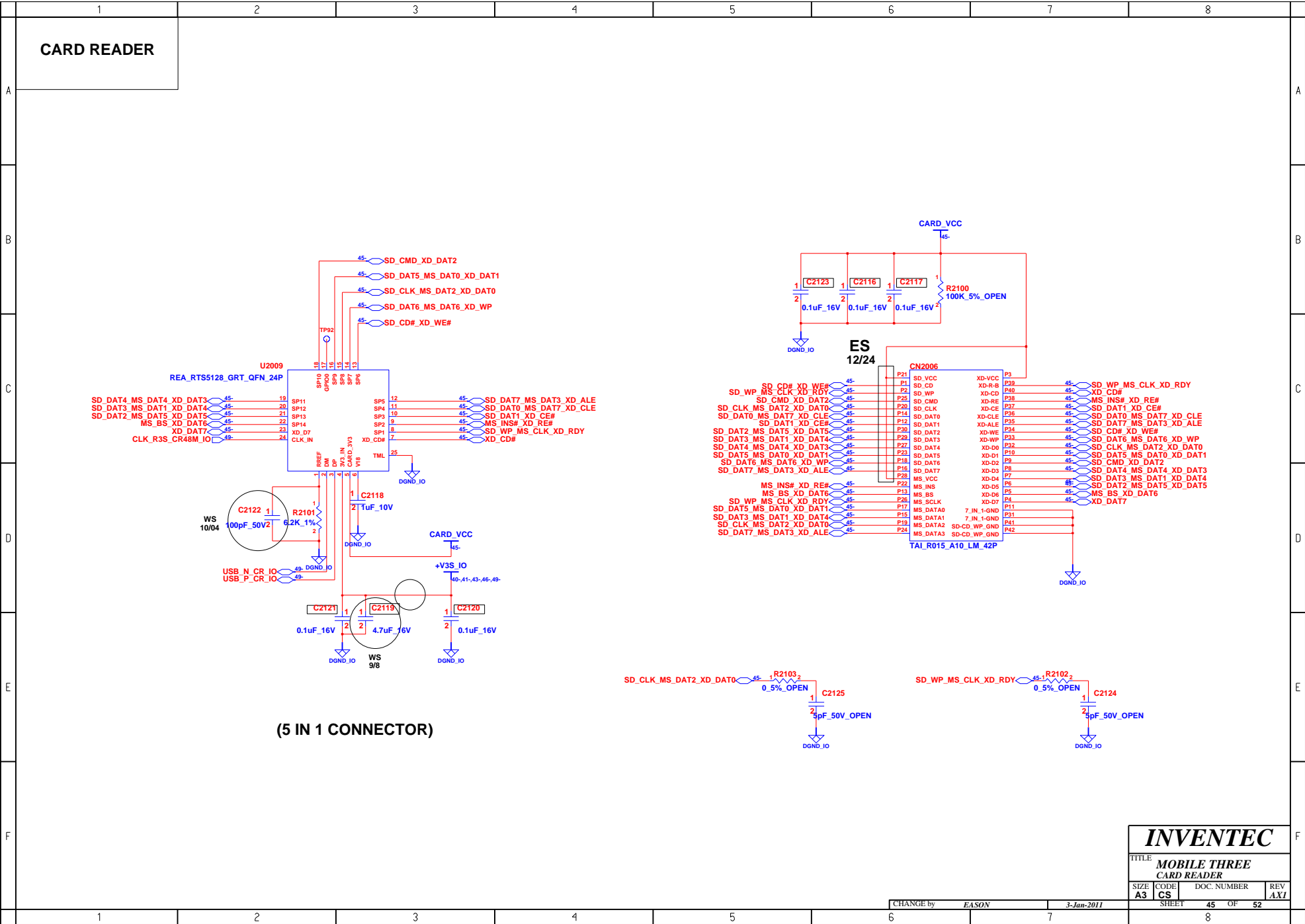
CHANGE by EASON 3-Jan-2011

WWAN



INVENTEC			
TITLE: MOBILE THREE			
WWAN			
SIZE: A3	CODE: CS	DOC. NUMBER: 43	REV: AXI
CHANGE by: EASON		3-Jan-2011	
SHEET: 43		OF: 52	
		8	



[illegible][illegible][illegible]

CARD READER

MOBILE THREE CARD READER

(5 IN 1 CONNECTOR)

INVENTEC

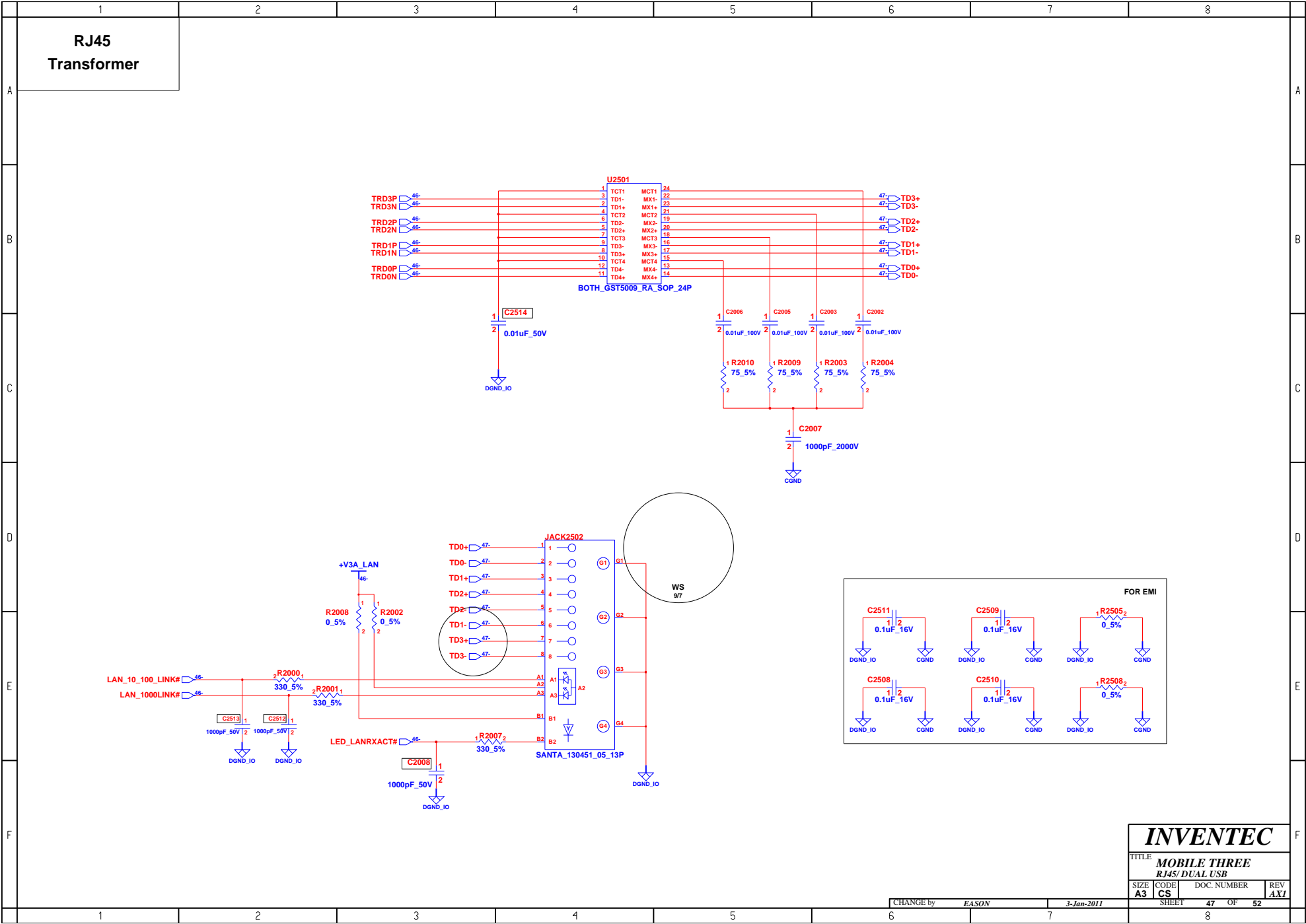
TITLE MOBILE THREE CARD READER

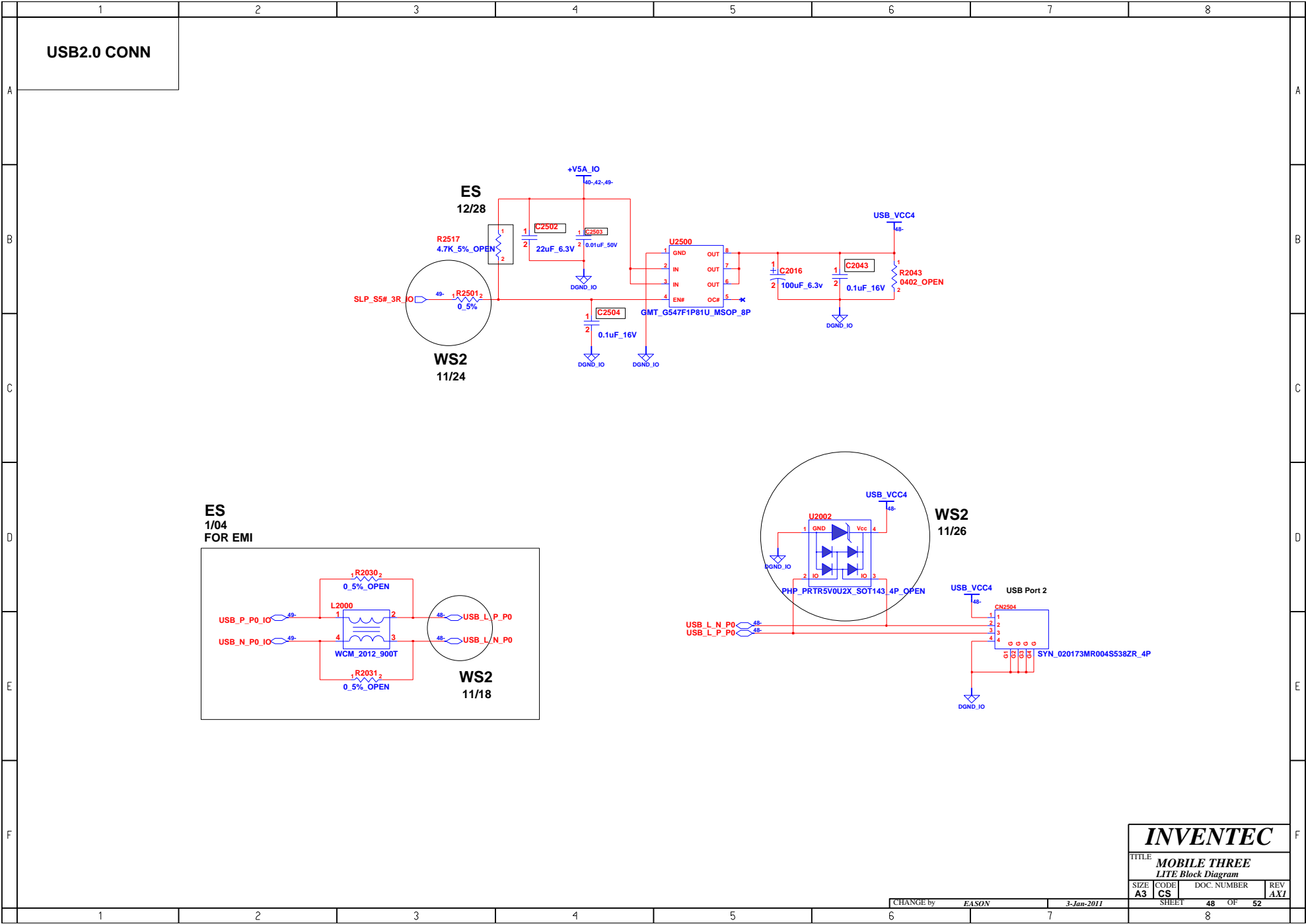
SIZE A3 **CODE** CS **DOC. NUMBER** 3-Jan-2011 **REV** AX1

SHEET 45 **OF** 52

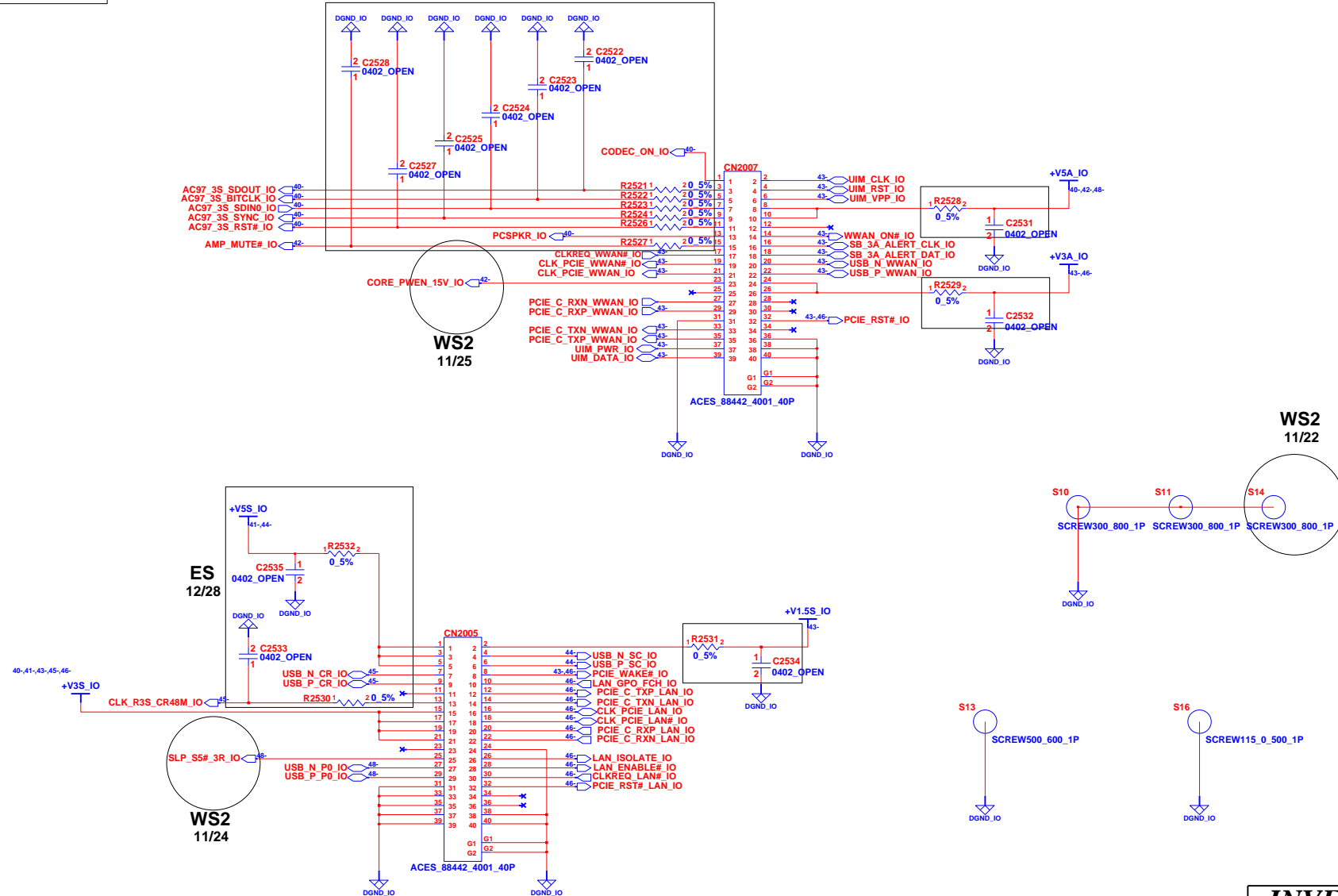
CHANGE by EASON 3-Jan-2011

[illegible][illegible][illegible]





MB TO I/O BOARD CONN
(I/O Board Side)



INVENTEC

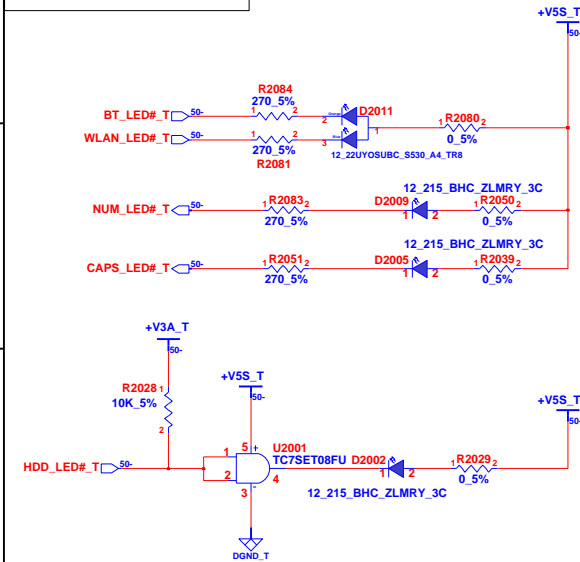
TITLE
**MOBILE THREE
DIAGRAM**

SIZE	CODE	DOC. NUMBER	REV
A3	CS		AXI

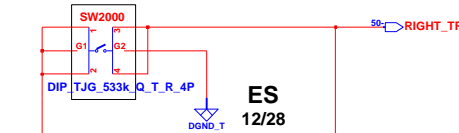
CHANGE by EASON 3-Jan-2011

SHEET 49 OF 52

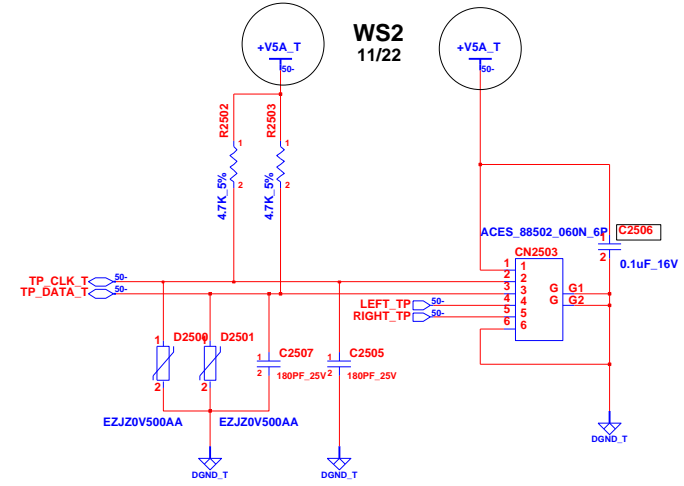
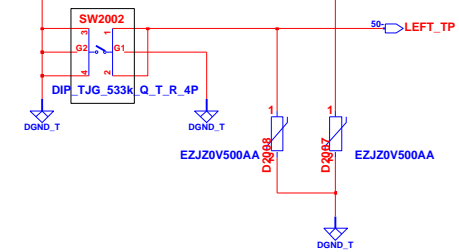
TOUCH PAD BOARD



Touch Tad Right key

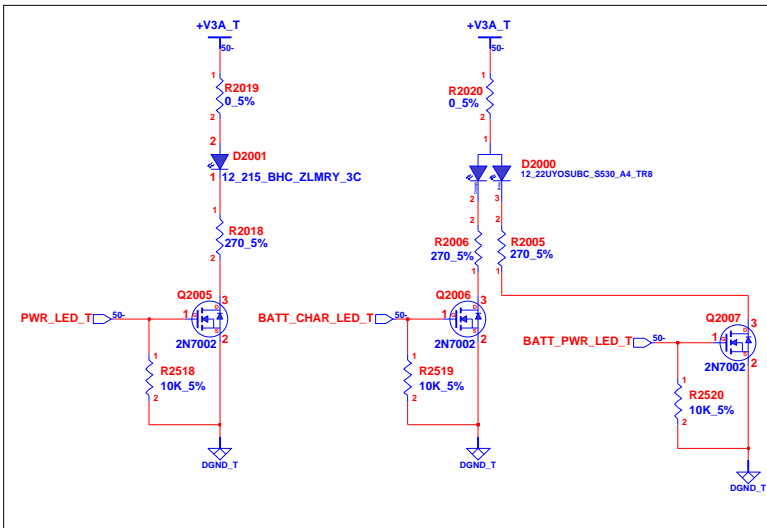


Touch Tad left key



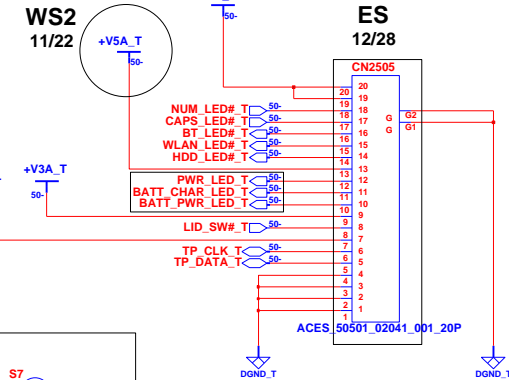
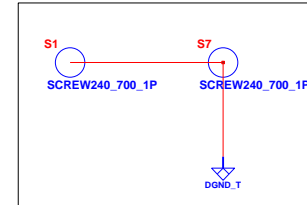
For Touch PAD Module

ES
1/3



WLAN_LED# & BT_LED#	PURPLE
WLAN_LED#	BLUE
BT_LED#	AMBER
NUM_LED#	BLUE
CAPS_LED#	BLUE
HDD_LED#	BLUE
PWR_LED#	BLUE
BATT_CHAR	AMBER
BATT_PWR	BLUE

ES
1/7



MB TO TP BOARD

INVENTEC

TITLE MOBILE THREE AUDIO JACK/ PWR SW			
SIZE A3	CODE CS	DOC. NUMBER	REV AXI

CHANGE by EASON 3-Jan-2011

SHEET 50 OF 52

POWER SWITCH BOARD

FOR EMI

+V3LA_P

1 R2507 10K_5%

2

D2501

D2502

DGND_P

ES 12/29

SW2001

DIP_TJG_533K_Q_T_R_4P

DGND_P

WS2 11/18

+V3LA_P

1

2

3

4

5

6

ENTERY_691BK_Q06M_00L_6P

DGND_P

POWER SWITCH BOARD TO MB

ES 1/4

+V3A_P

1

2

R2504 0_5%

ES2 02/08

D2003

1

2

113_BHC_YL2M2QY_3T

1

2

R2506 270_5%

Q2008

1

2

2N7002

PWR_LED#_P

1

2

R2544 10K_5%

DGND_P

ES 1/7

S3

SCREW240_700_1P

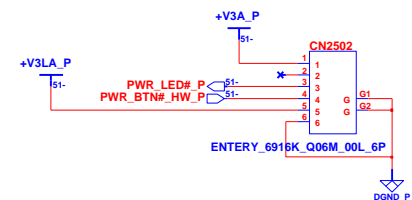
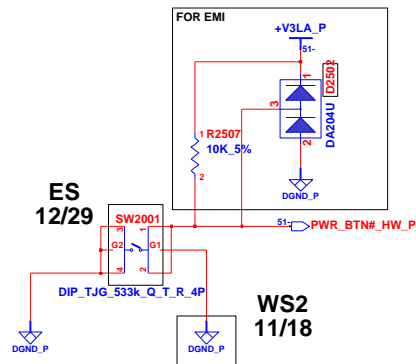
S5

SCREW240_700_1P

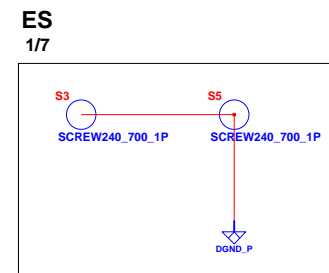
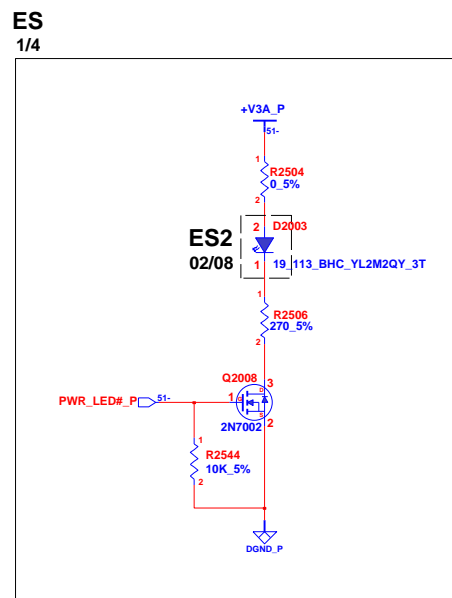
DGND_P

INVENTEC			
TITLE: MOBILE THREE USB CONN.			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	51 OF 52	AXI

CHANGE by EASON 3-Jan-2011



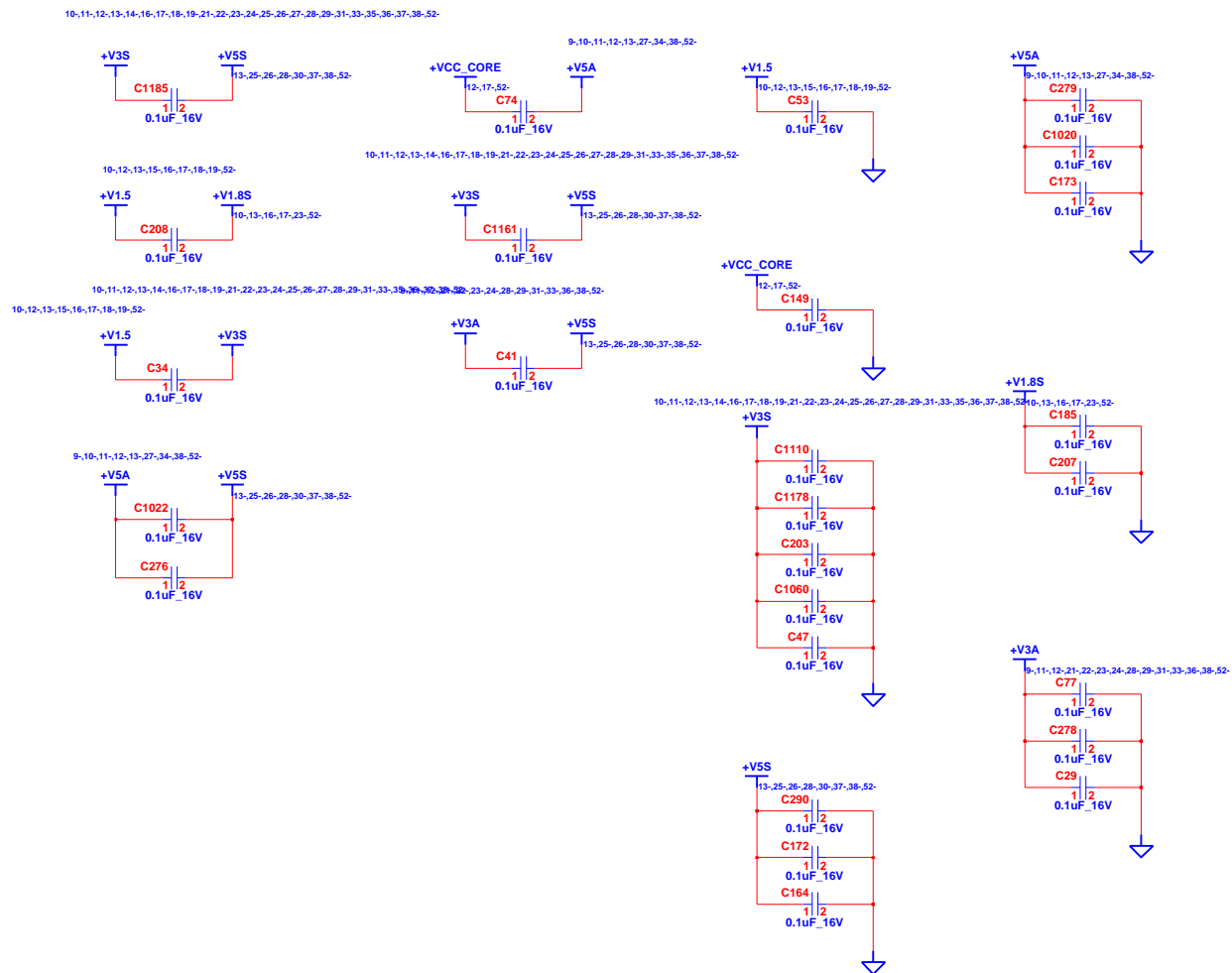
POWER SWITCH BOARD TO MB



TITLE			
MOBILE THREE USB CONN.			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		AXI
SHEET 51 OF 52			

CHANGE by		EASON	3-Jan-2011	A3	CS	SHEET	51	OF	52	AXI
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EMI



INVENTEC

TITLE			
MOBILE THREE			
EXTENDED block diagram			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		AXI

CHANGE by EASON 3-Jan-2011

SHEET 52 OF 52